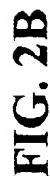


FIG. 1





**FIG. 2B**

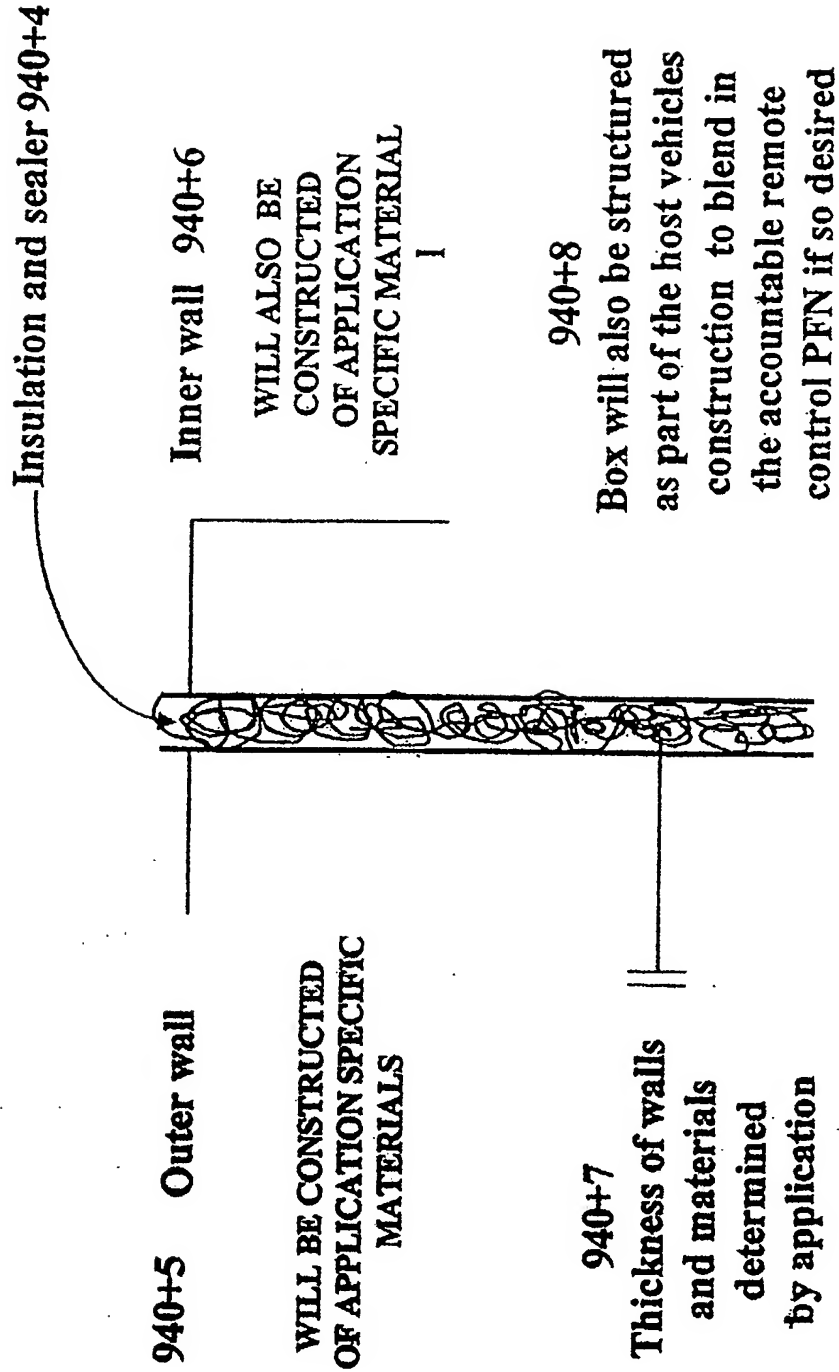


FIG. 2C

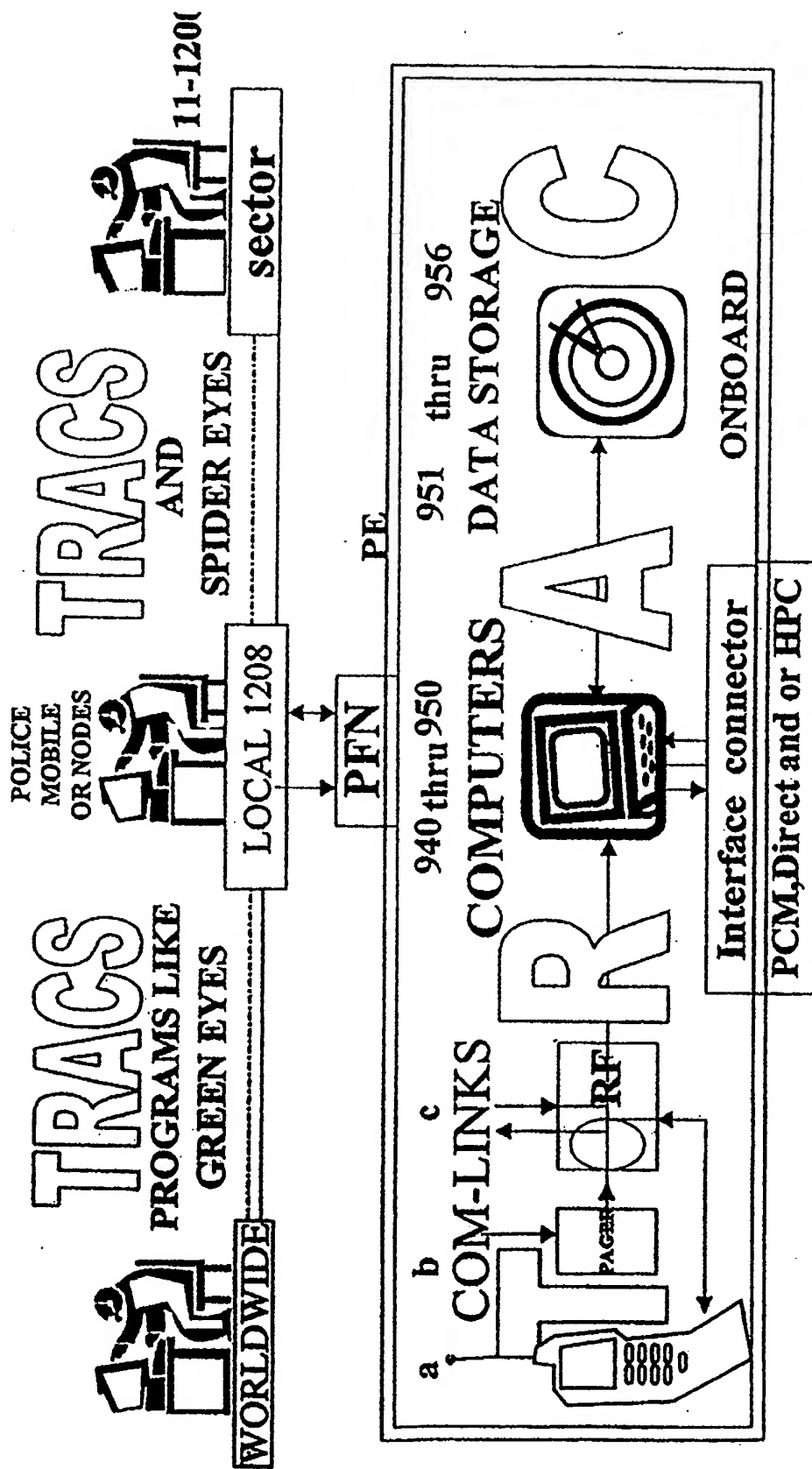


FIG. 2D

```

init:
x var word
tflag var bit
relay var bit
solenoid var bit
switch var bit
tflag=0
relay=0
switch=0
solenoid=0
input 12
input 14
input 15
output 4
outout 1
output 2
output 3
output 5
output 6
output 7
output 8
output 9
output 10
output 11
output 13
out1=0
out2=0
out3=0
out4=0
out5=0
out6=0
out7=0
out8=1
out9=0
out10=1
out11=0
out13=0
start:
if in15=1 then init reset ID sys.
if in14=1 then check1
check1:
if tflag=1 and in 12=0 then relaye
check2:
if in12=1 and solenoid=0 then carrun
check3:
if in 12=1 and solenoid=1 and tflag=1 then slowdown
check4: go to start

check1:
for x=1 to 35
pause 100
if in 14=0 then nogo
next
goto go
nogo:
goto check1
go:
tflag=1
debug "go condition",cn
goto check1

noactivity:
debug "Beeper is Inactive", cr
return

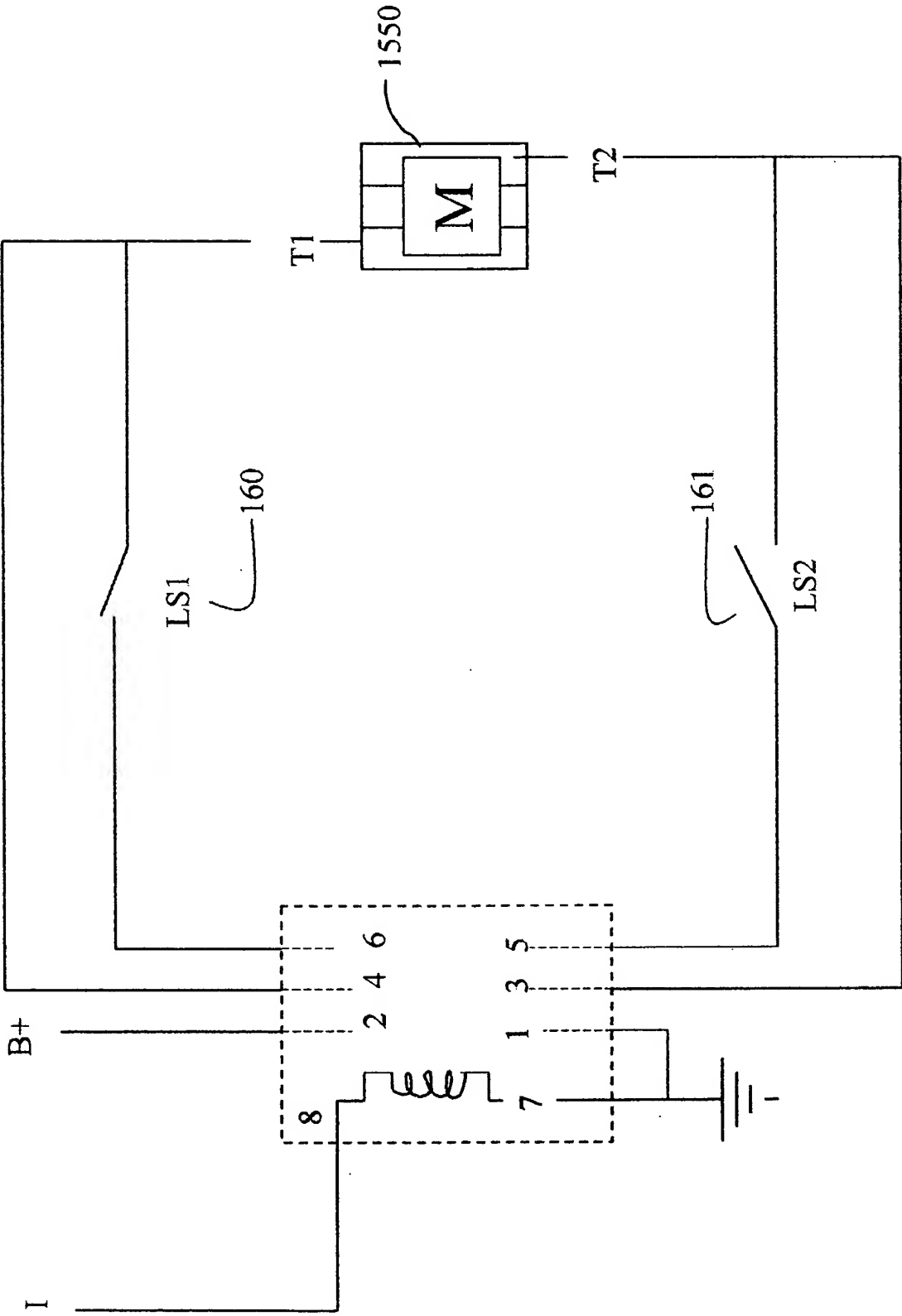
relayc:
debug "relay control"
debug ? relay
if relay=0 then first
if relay=1 then second
first:
debug "first"
out4=1
out2=1
out3=Pause 2,300 RS out4=0
relay=1
goto relend
second:
debug "second"
out2=0
out5=1
pause 3000
out5=0
relay=0
tflag=0
hold: if in 14=1 then
hold
stuck1: if in 15=0 then
stuck 1
goto init
relend:
tflag=0
hold1: if in 14=1 then hold 1
goto check 2

carrun:
out1=0
out2=0
out3=0
solenoid=1
goto check 3

slowdown:
if switch=0 then fir
if switch=1 then sec
fir:
out1=1 accelerator diseng.
siren-flasher
out8=0 play message
out9=1 amp on for play
pause 15000
out8=1 off record chip
out 10=0 reset recorder chip
pause 1000
out10=1 reset ready
pause 21000
out2=1 progressive brake
application
switch=1
goto swend
sec:
out3=1 energize kill
out5=1
pause 3000
out5=0
pause 45000
out9=0
switch=0
tflag=0
hold3: if in14=1 then hold
3
stuck2: if in 15=0 then
stuck 2
goto init
swend:

```

FIG. 3



**FIG.4**

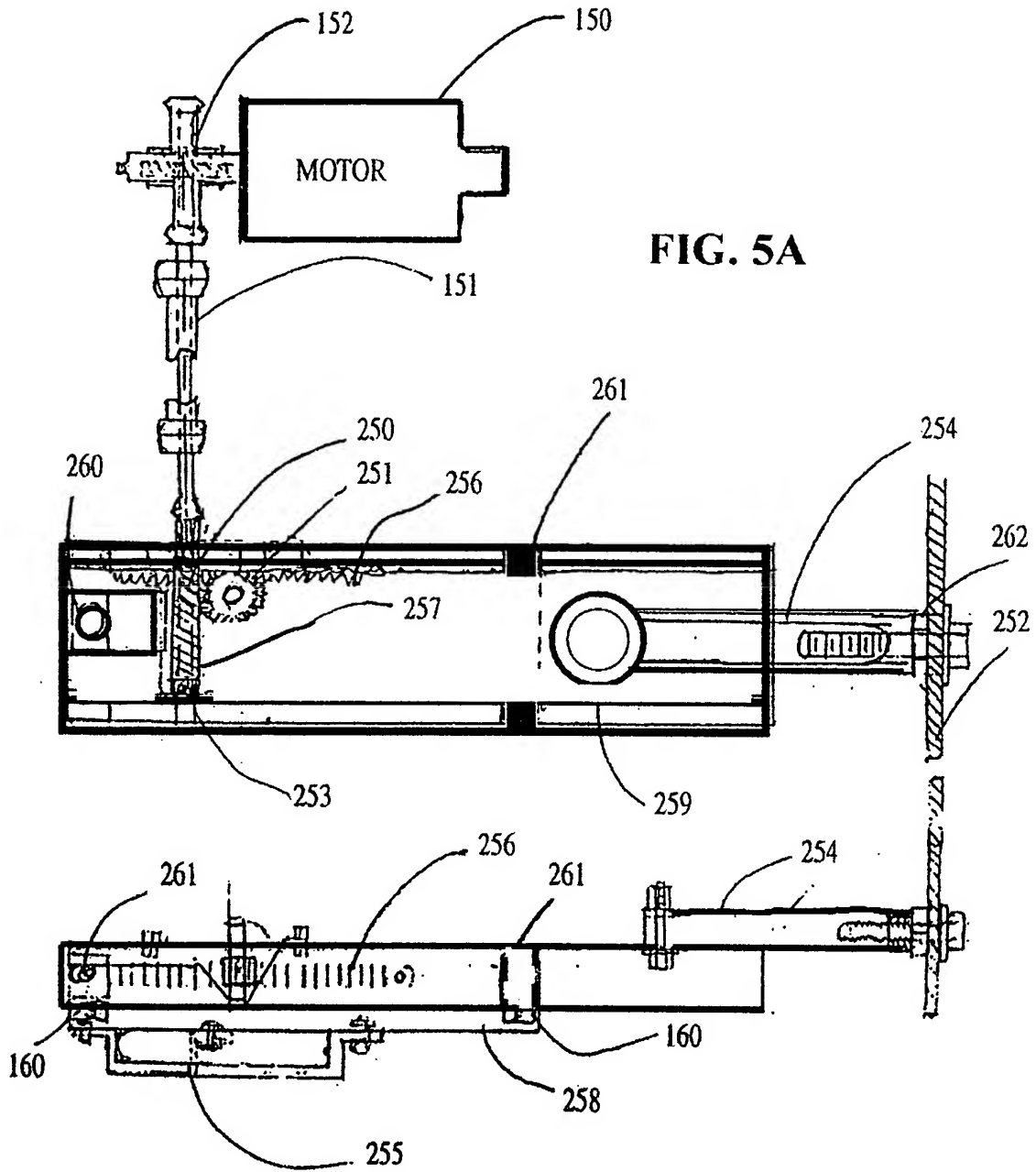


FIG. 5B



FIG. C

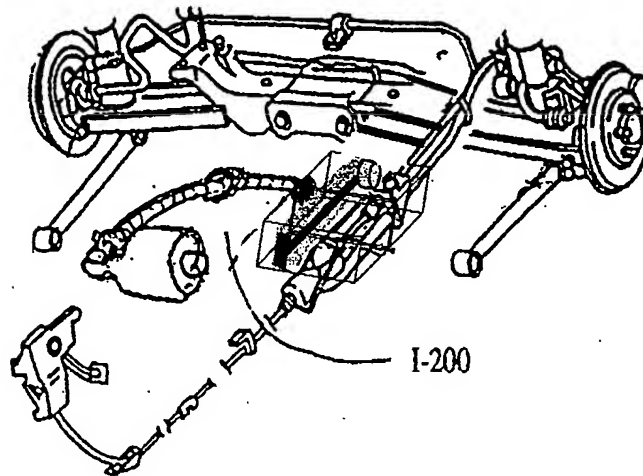


FIG. 5D

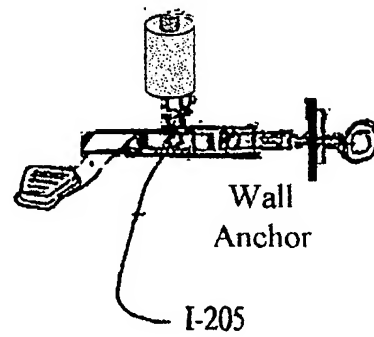
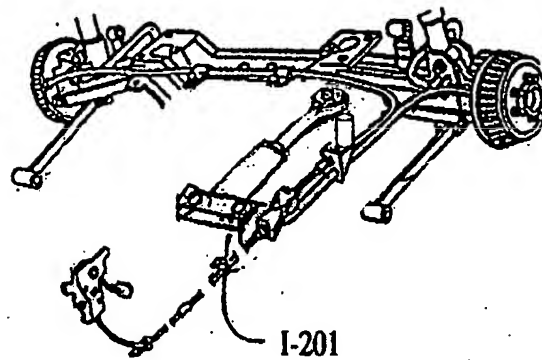


FIG. 5E



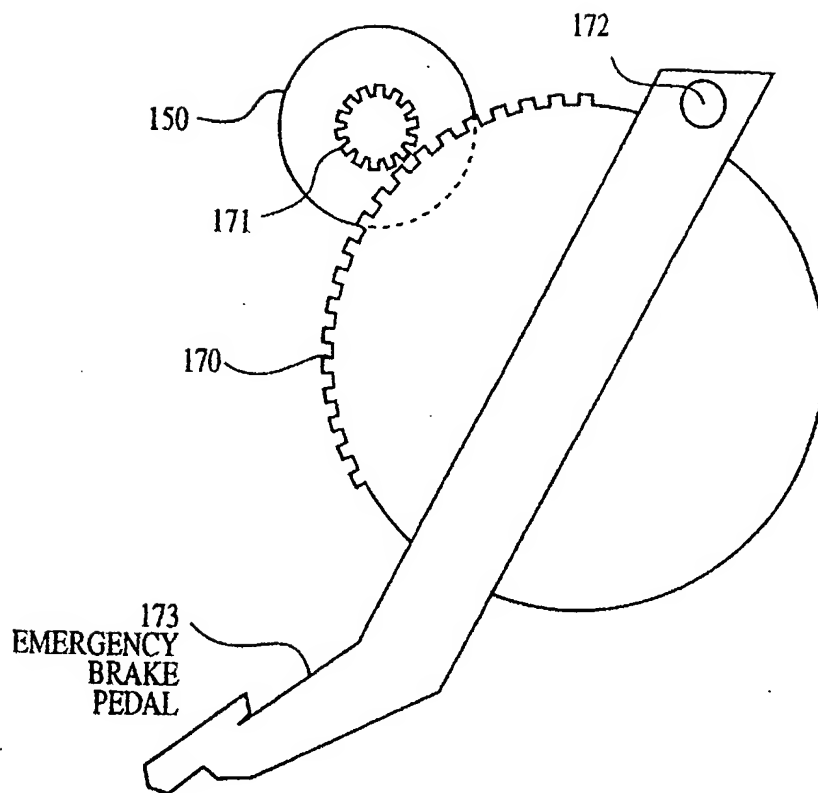
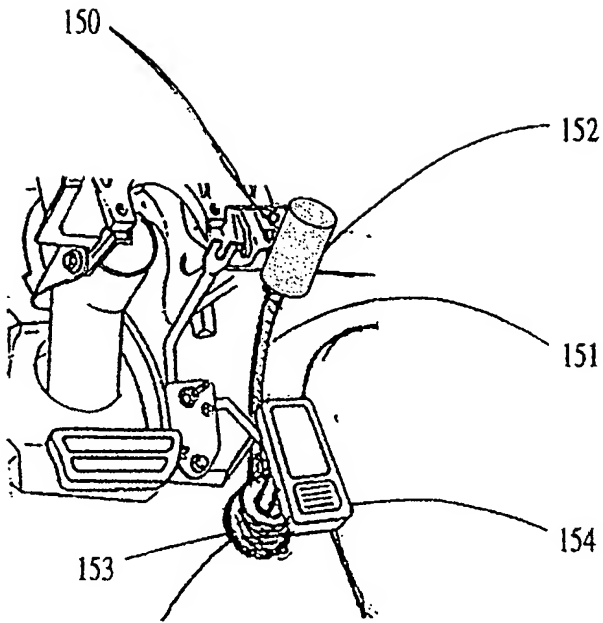
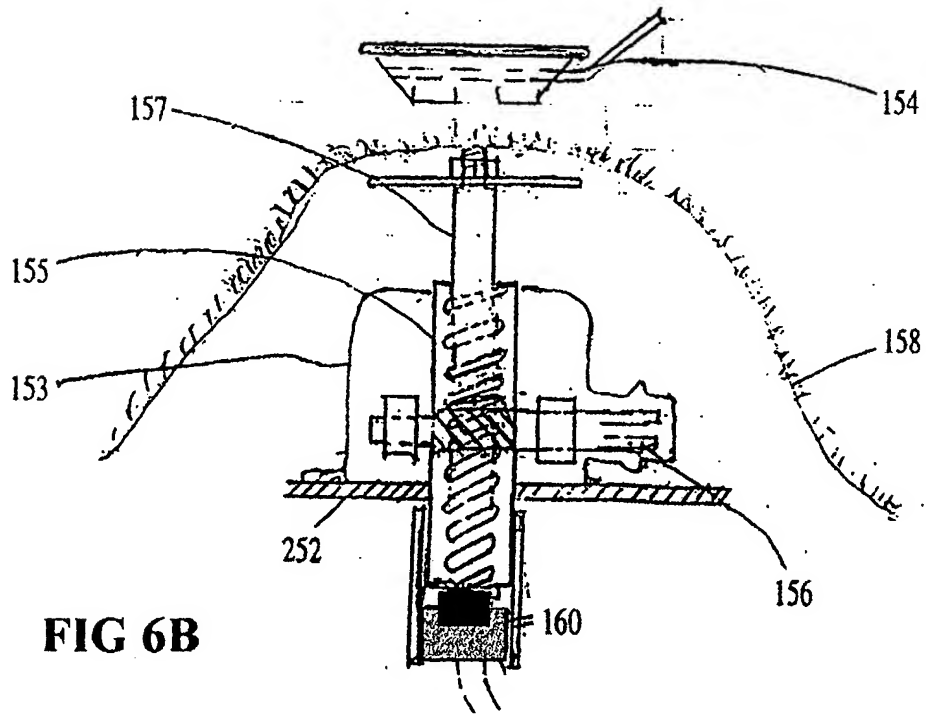


FIG. 5F



**FIG. 6A**



**FIG 6B**

FIG. 7A

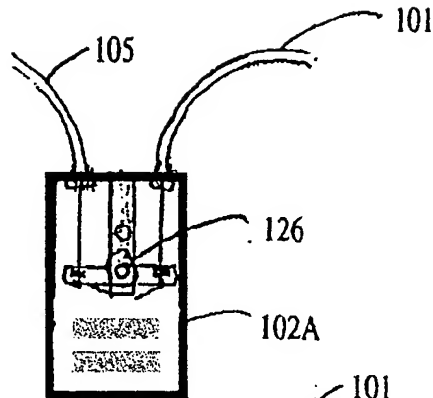


FIG. 7B

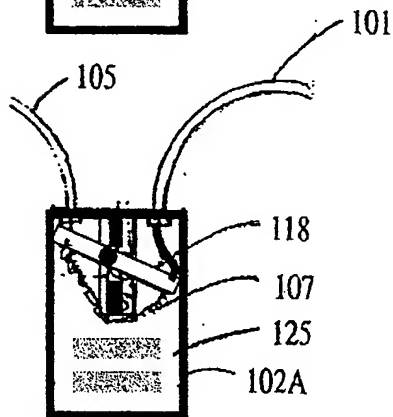


FIG. 7C

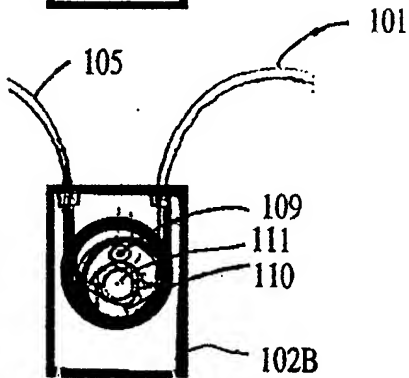
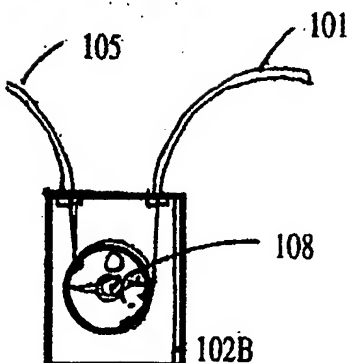


FIG. 7D



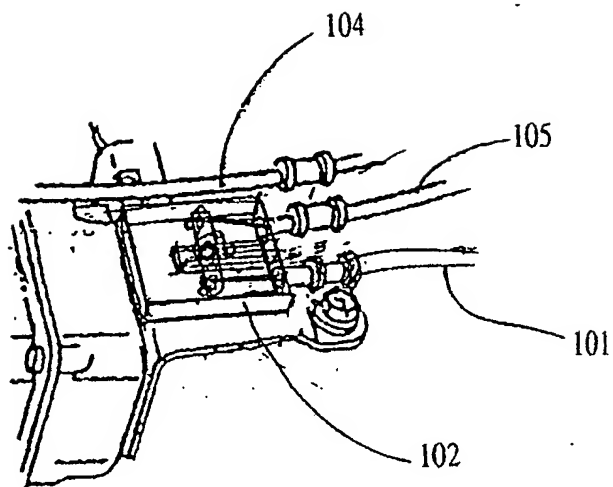


FIG. 7E

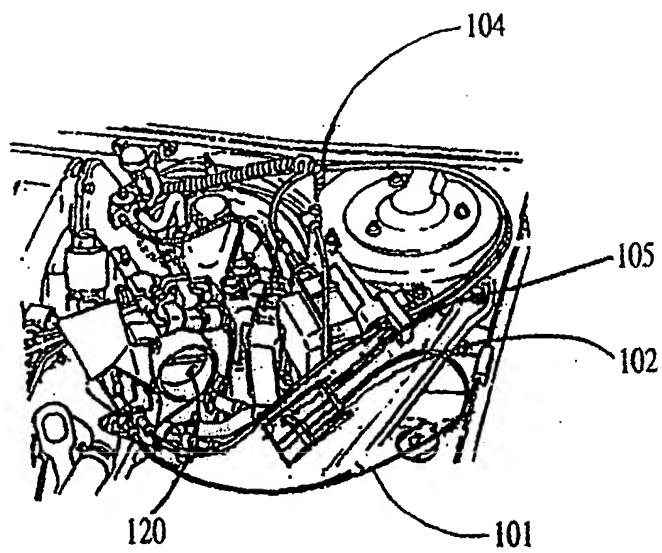


FIG. 7F

FIG. 8A

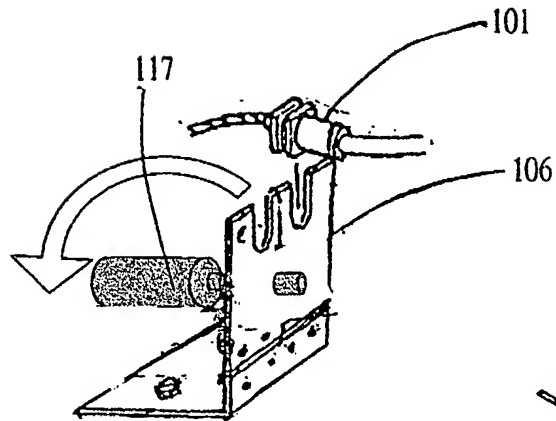


FIG. 8B

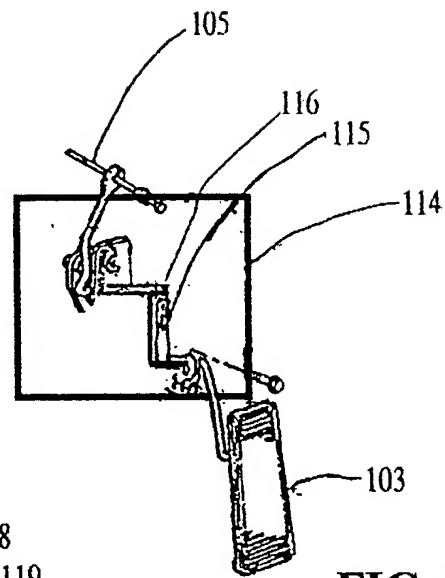


FIG. 8C

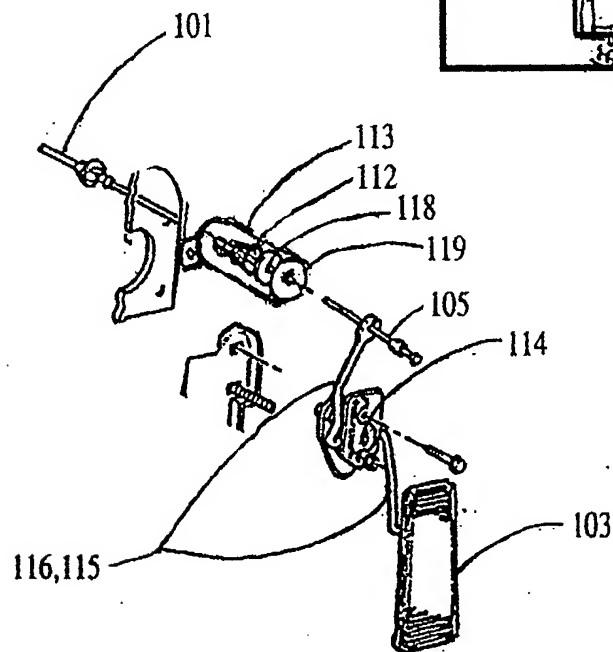


FIG. 8C

FIG. 9A

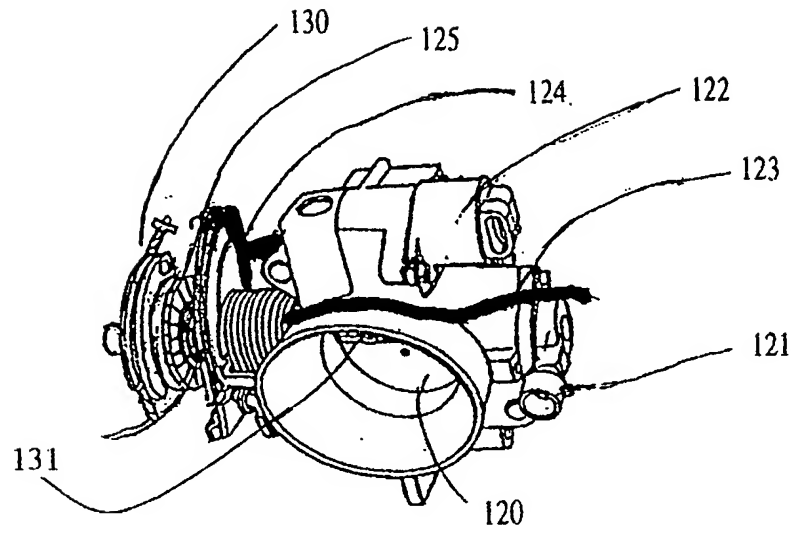


FIG. 9B

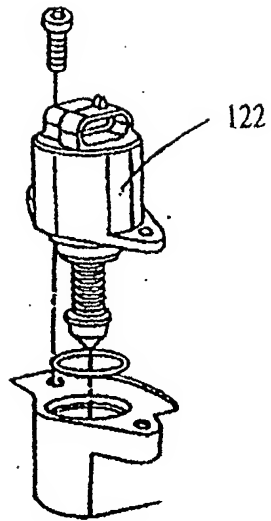


FIG. 9C

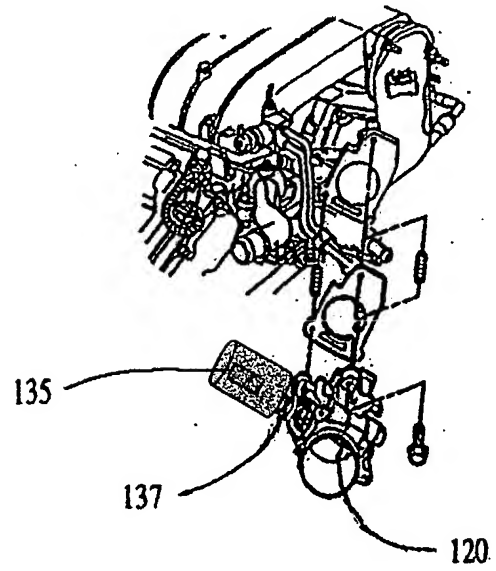


FIG. 10A

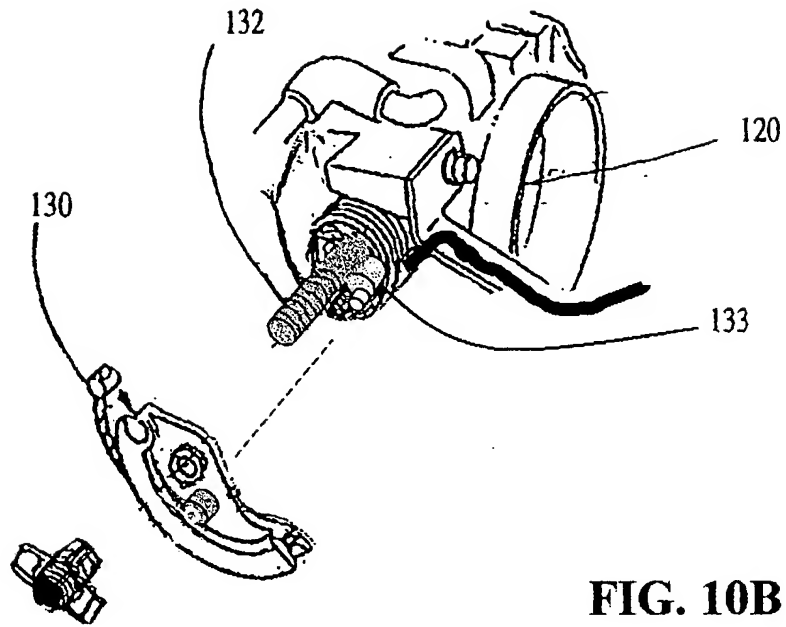


FIG. 10B

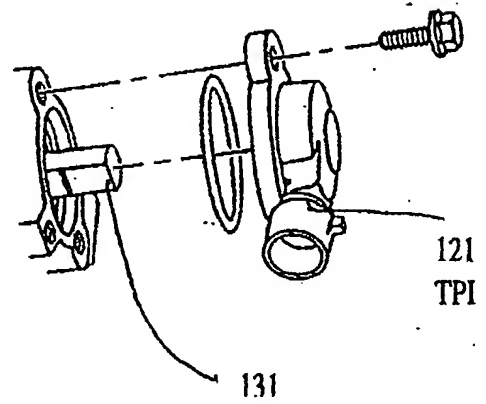


FIG. 10C

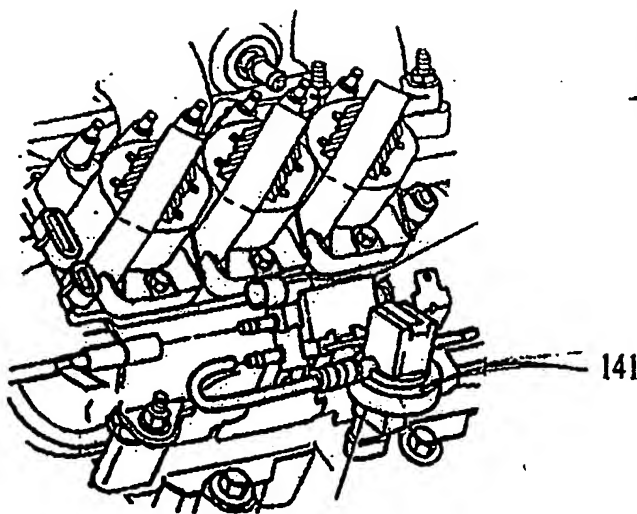




FIG. 11A

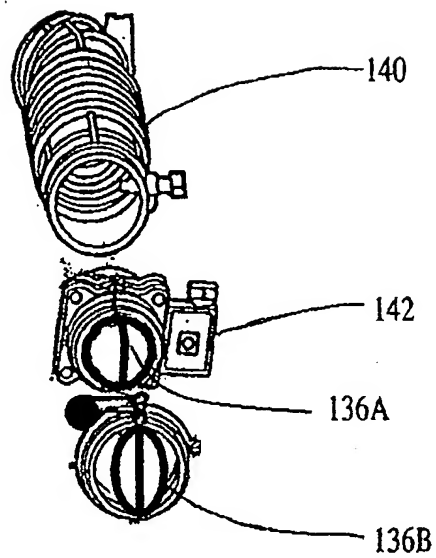
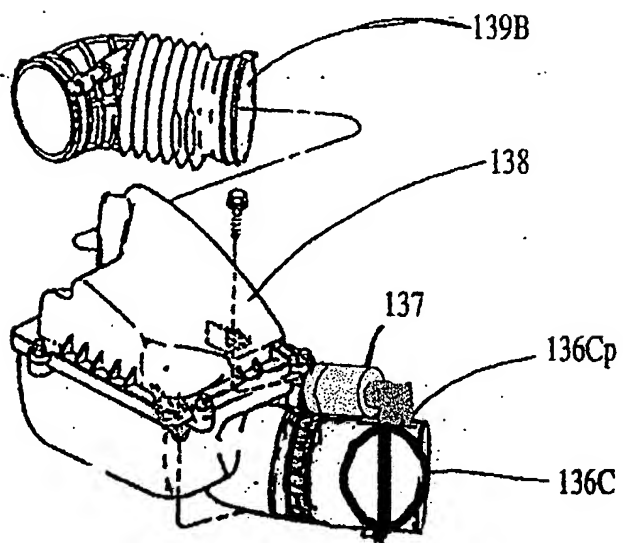
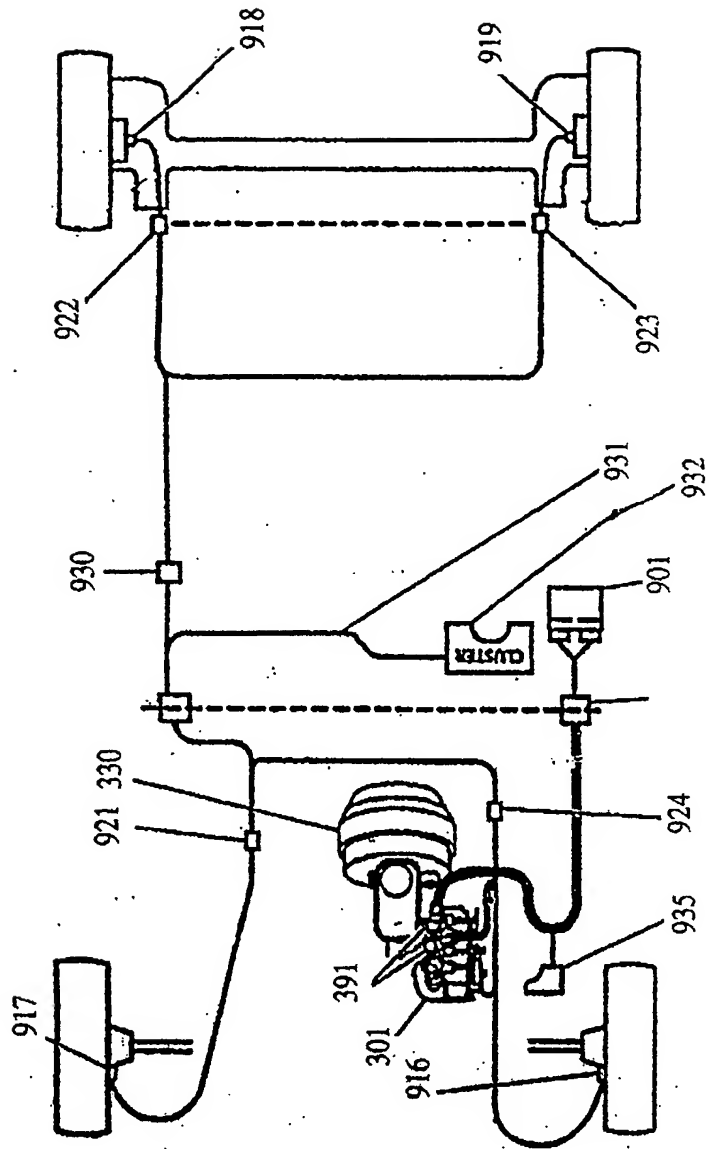


FIG. 11B





**FIG. 12A**

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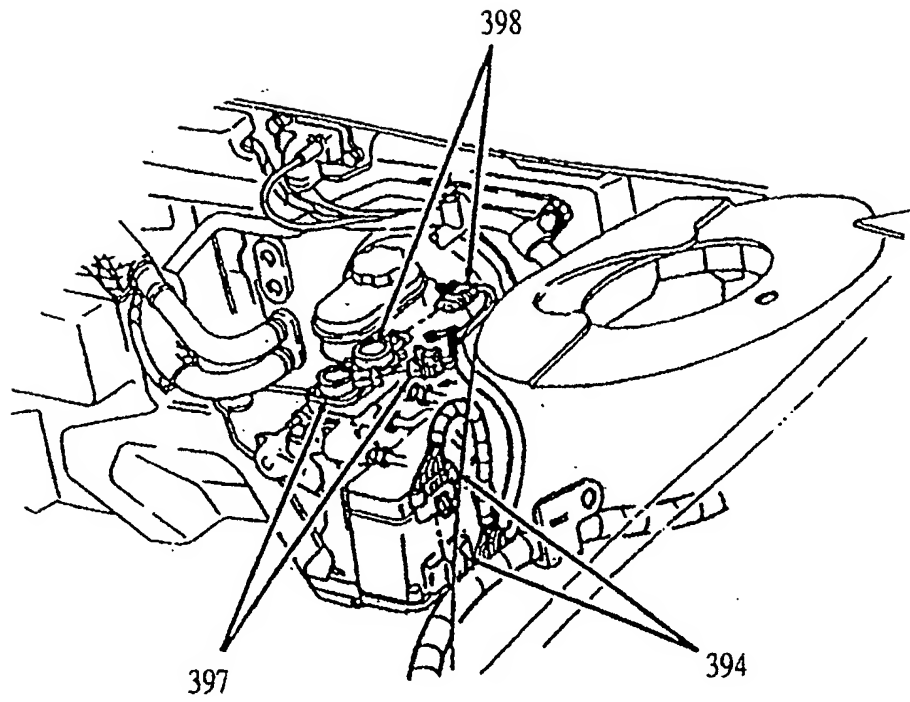


FIG. 12B

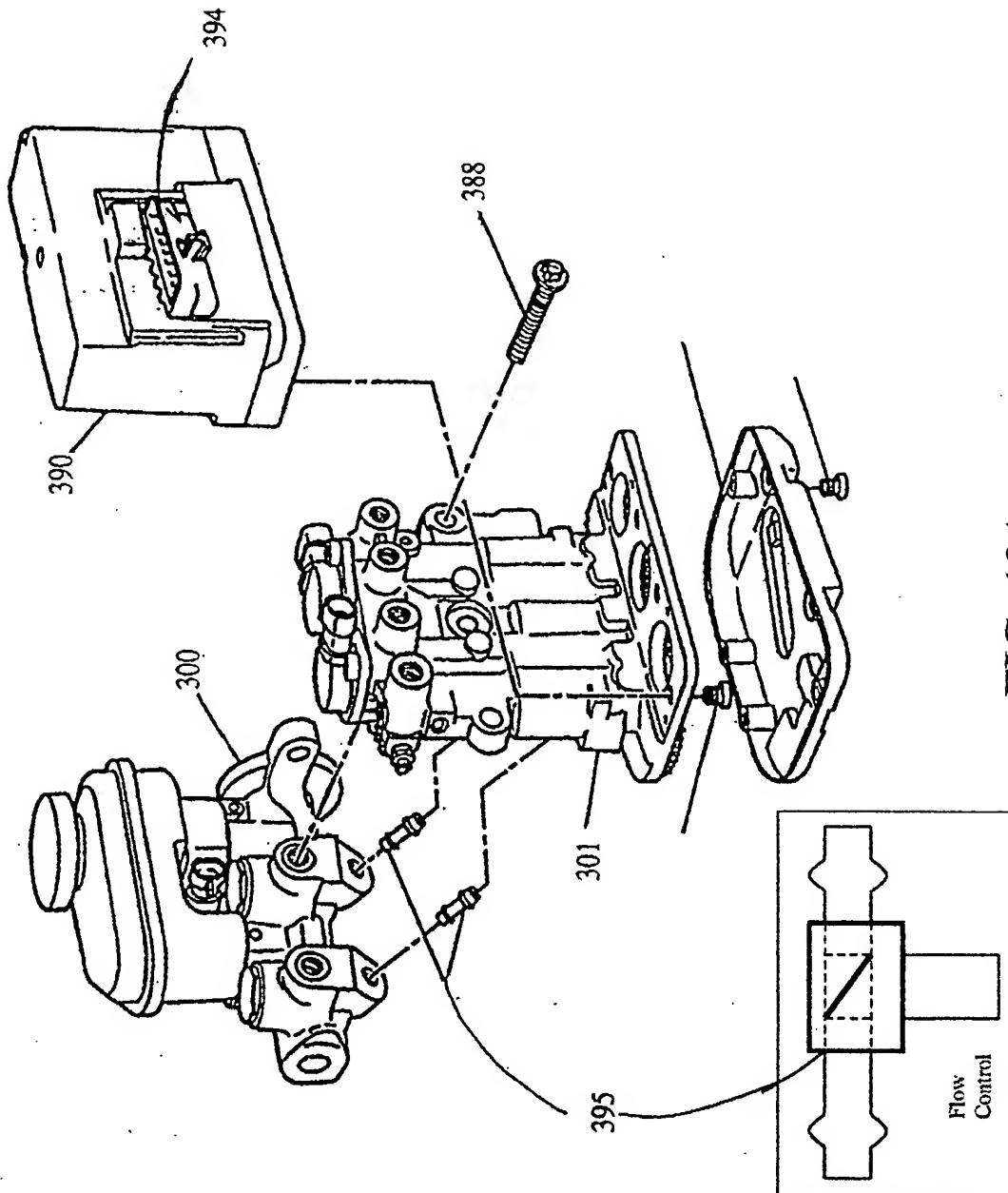
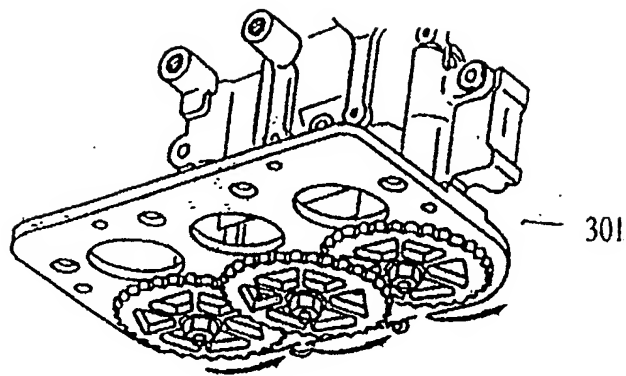


FIG. 13A

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**FIG. 13B**

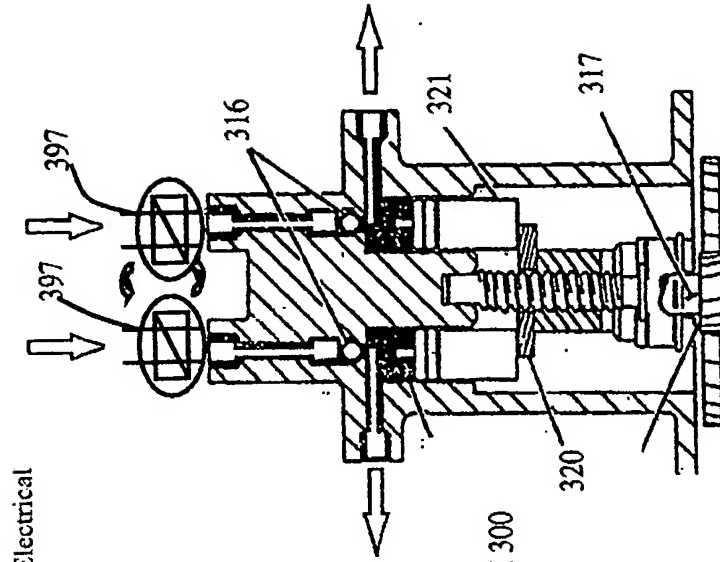


FIG. 14C

Electrical

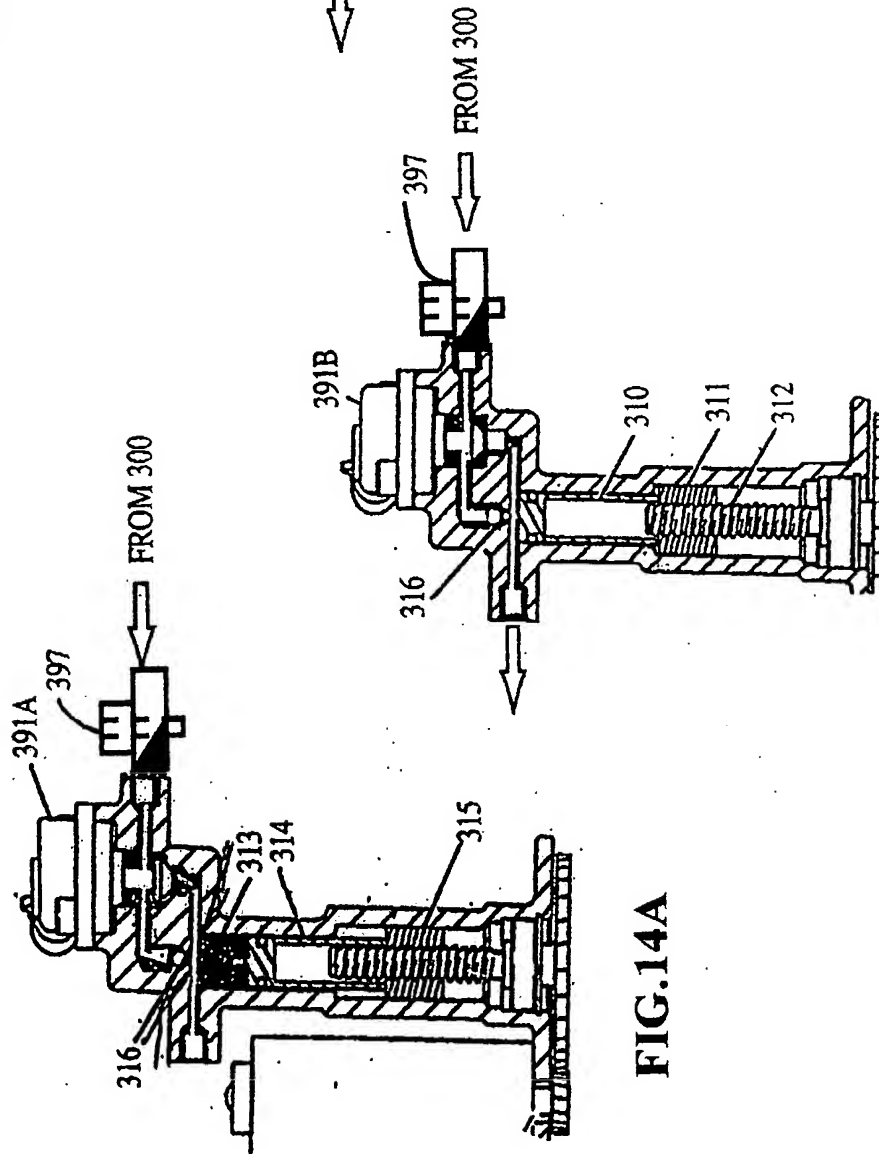
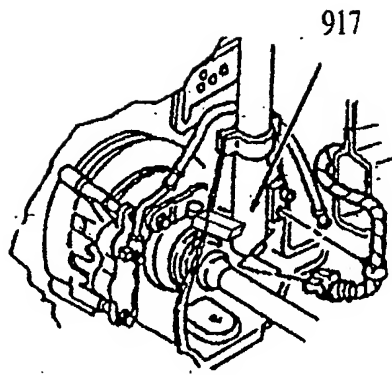
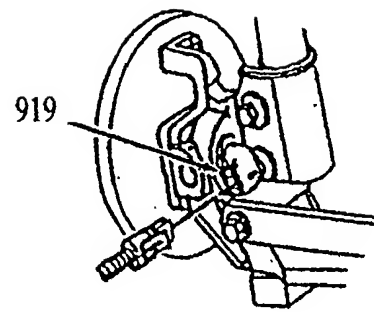


FIG. 14A

FIG. 14B



**FIG. 14D**



**FIG. 14E**

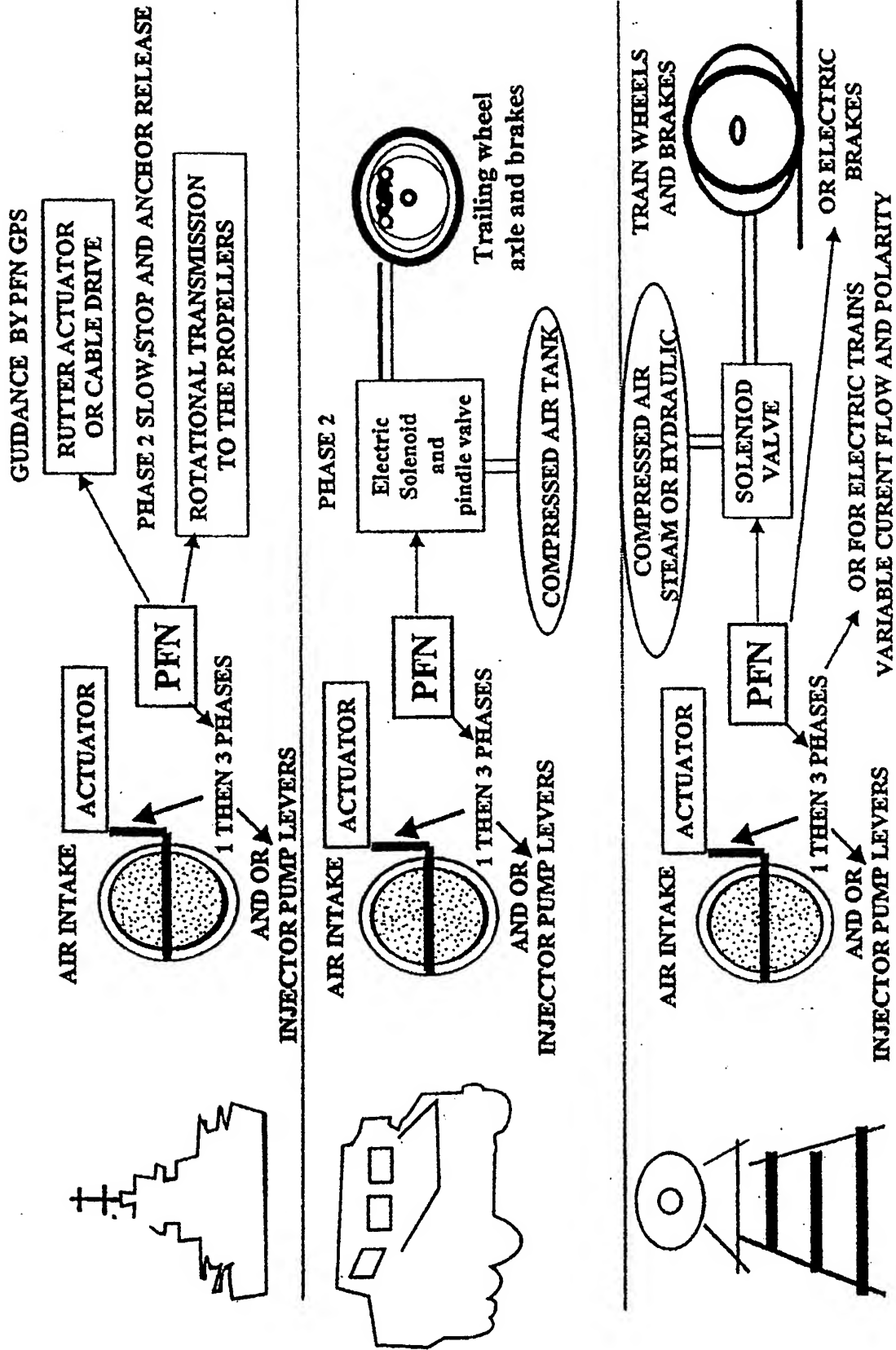


FIG. 14F



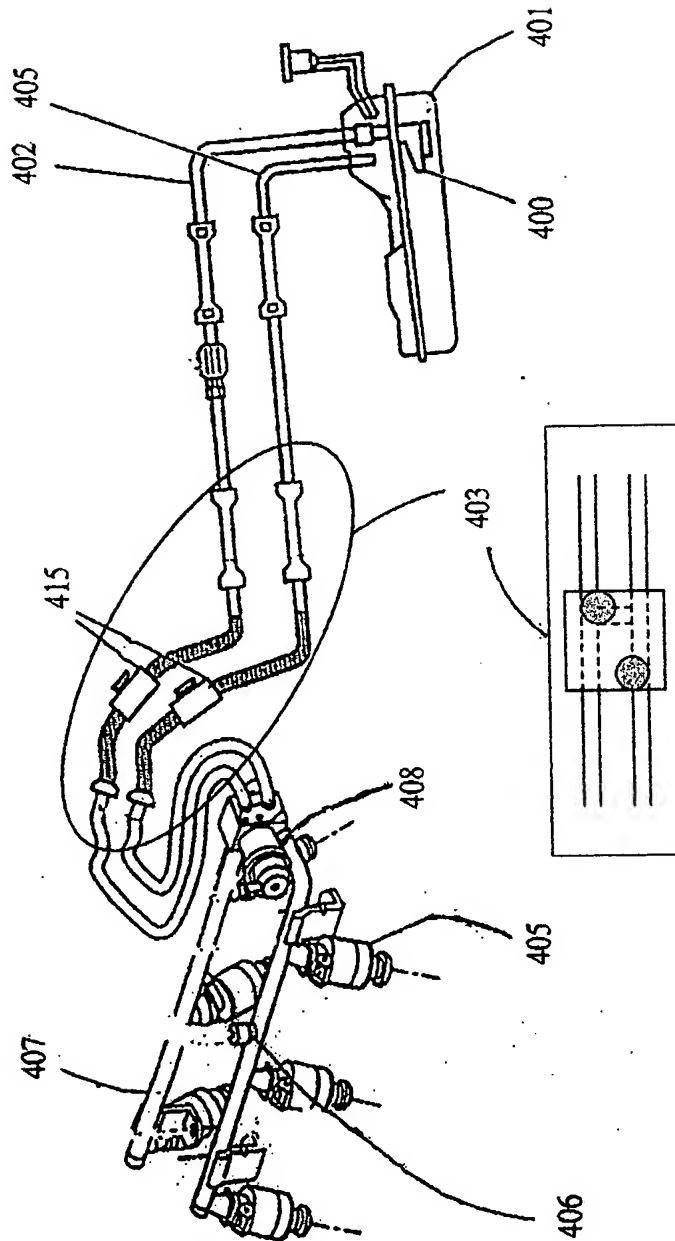
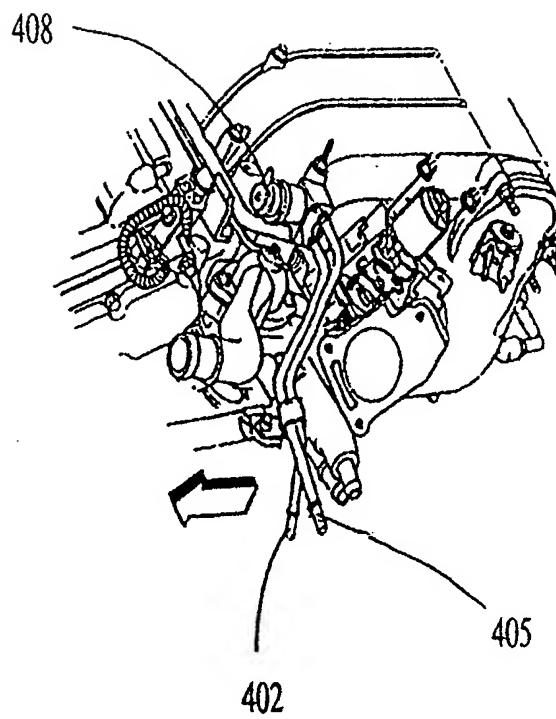


FIG. 15A



**FIG. 15B**

FIG. 16A

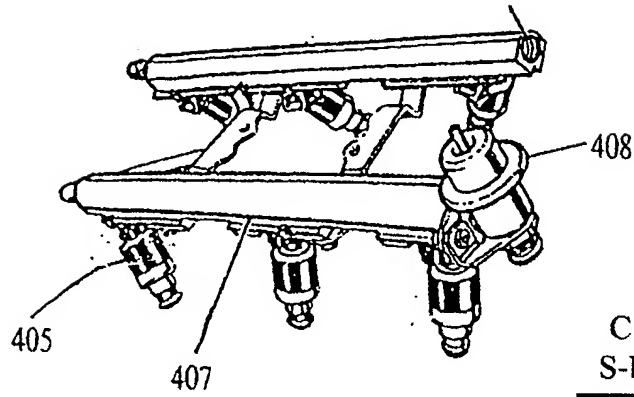


FIG. 16B

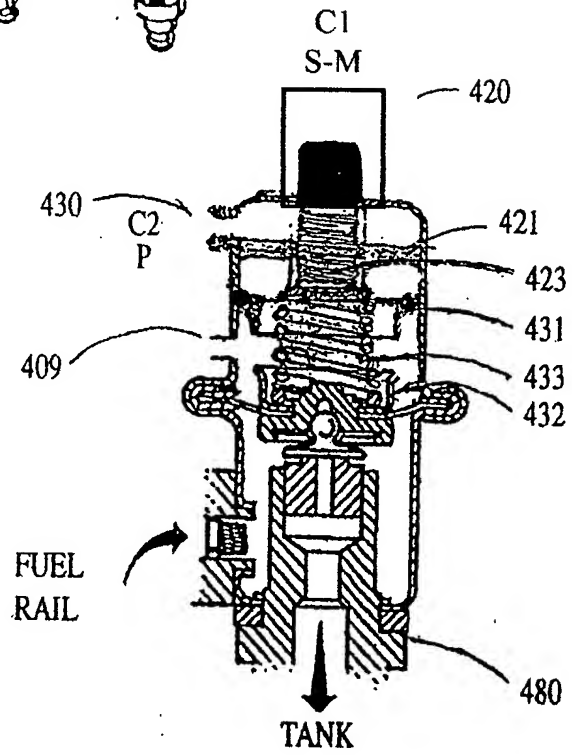
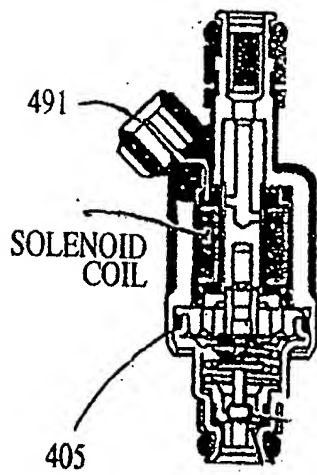


FIG. 16C

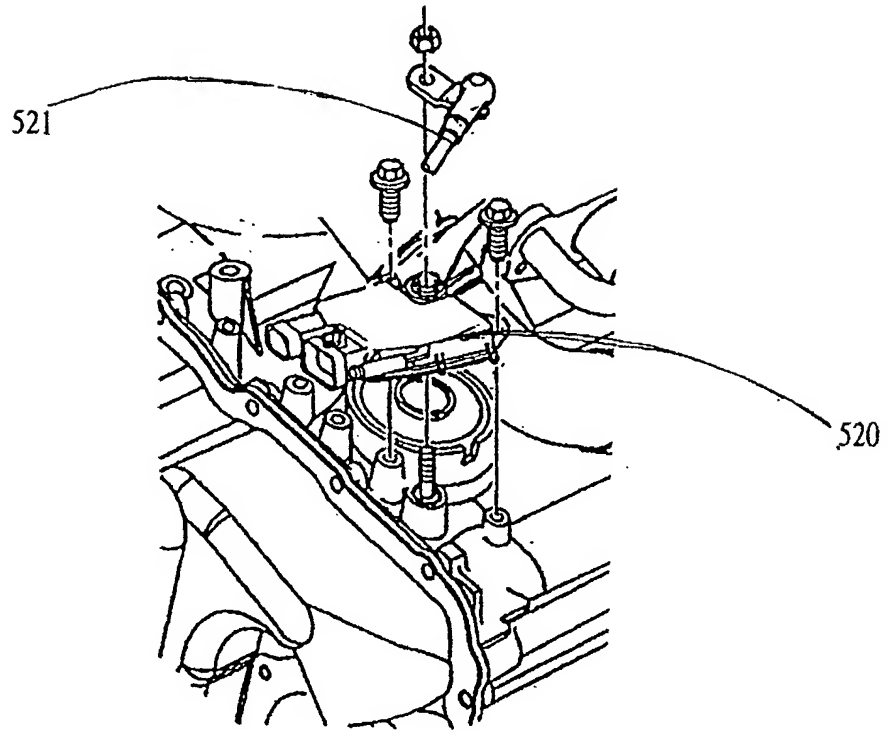


FIG. 17A

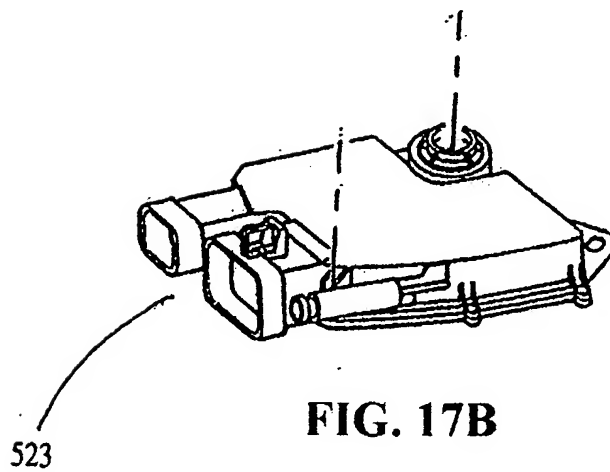


FIG. 17B

FIG 18A

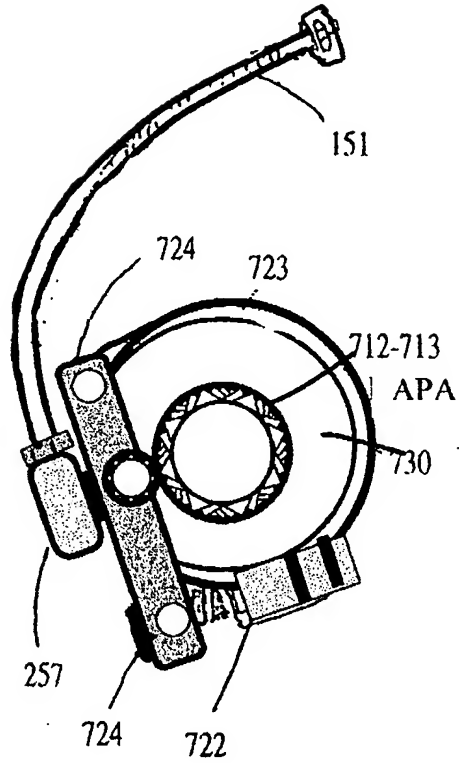


FIG. 18B

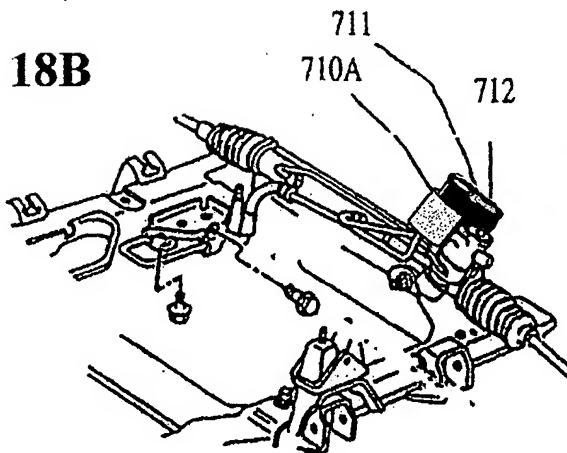


FIG. 18C



FIG. 18D

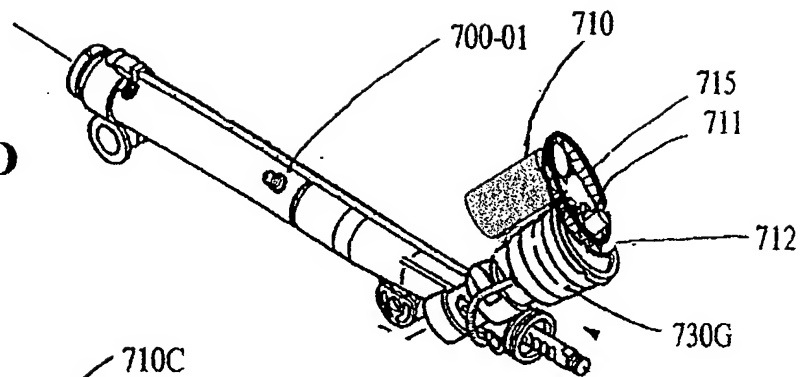


FIG. 18F

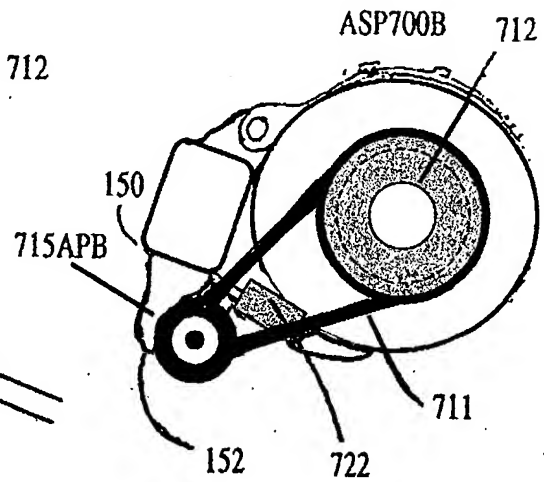


FIG. 18E

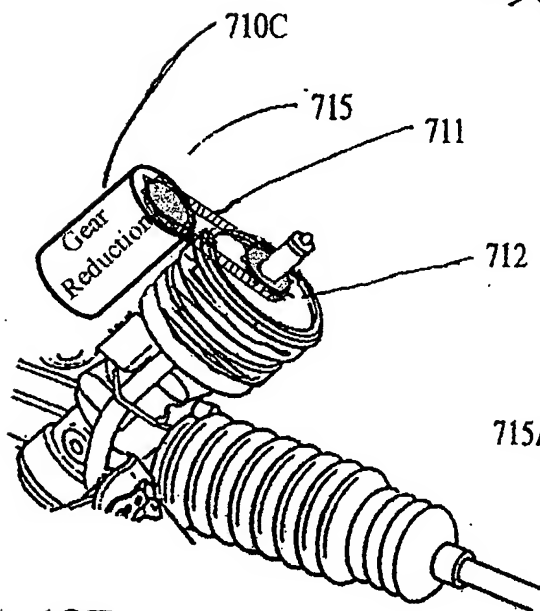




FIG. 19B

FIG. 19B

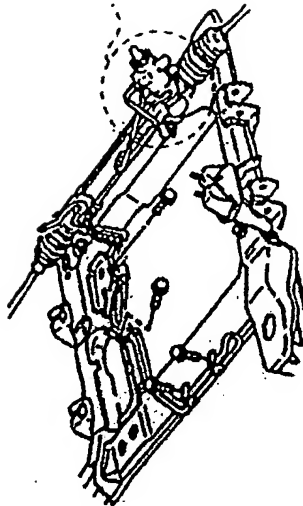


FIG. 19A

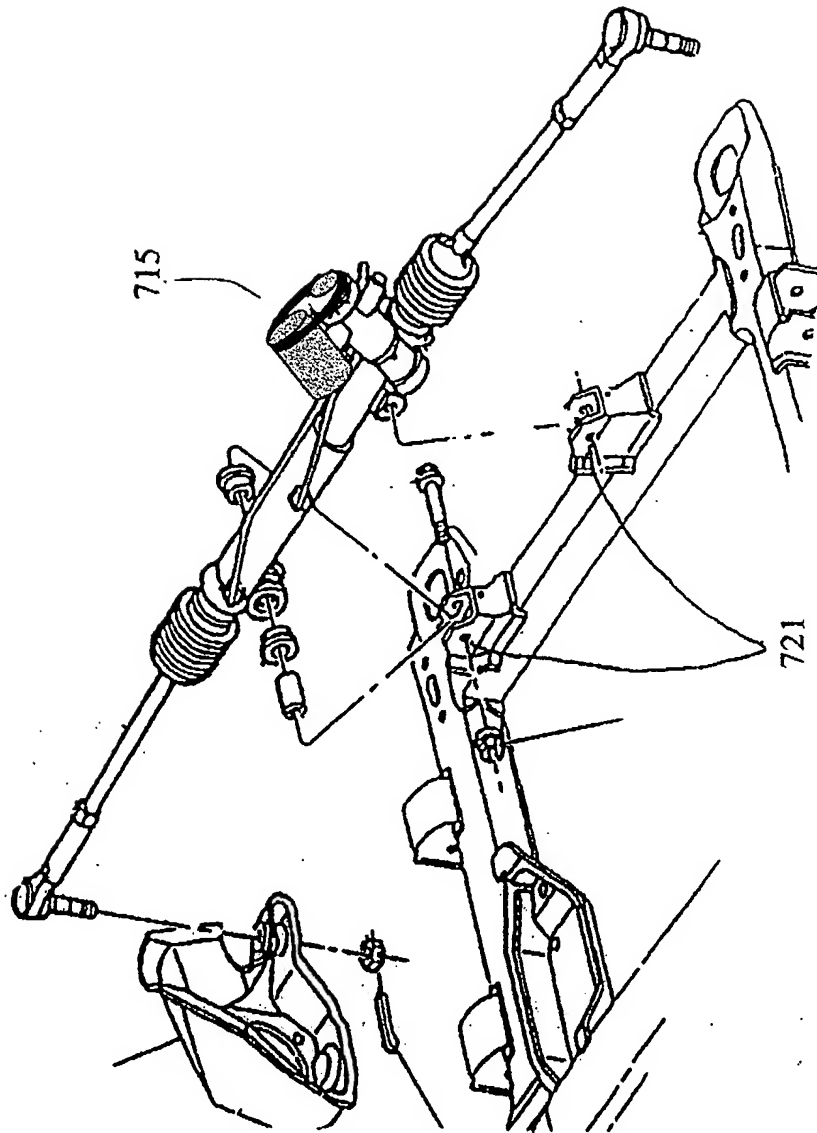


FIG. 19C



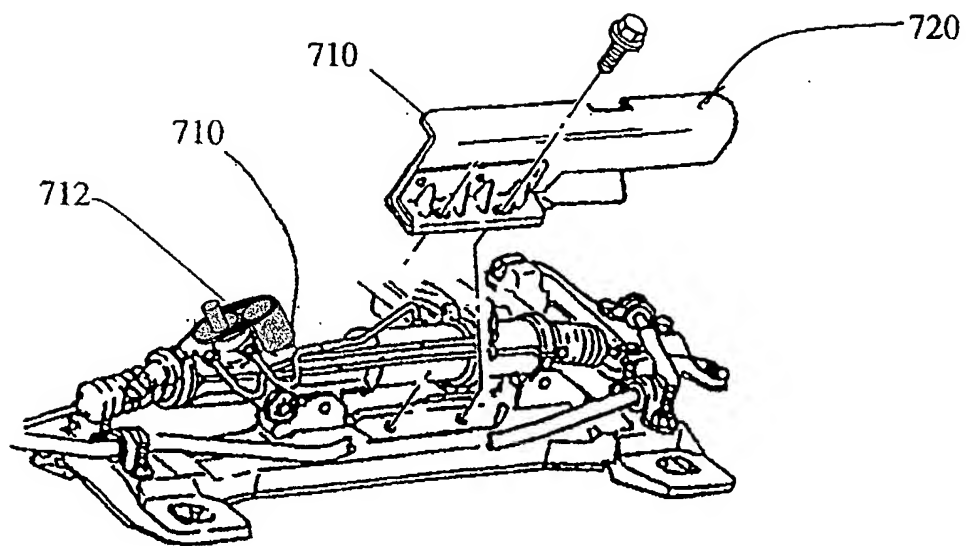


FIG. 19D

FIG. 20A

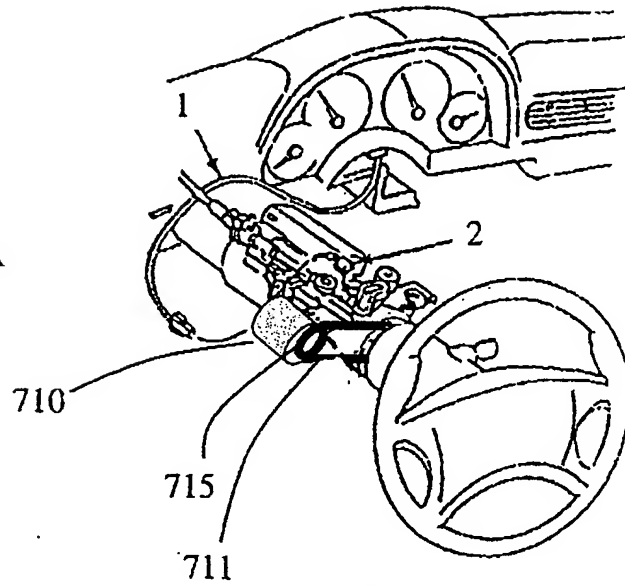


FIG. 20B

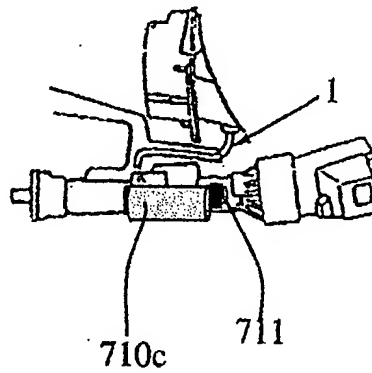
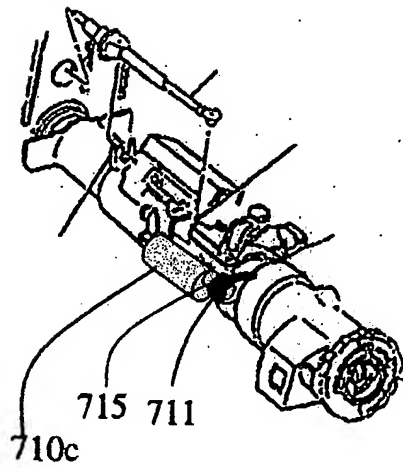


FIG. 20C



ASP700Bc

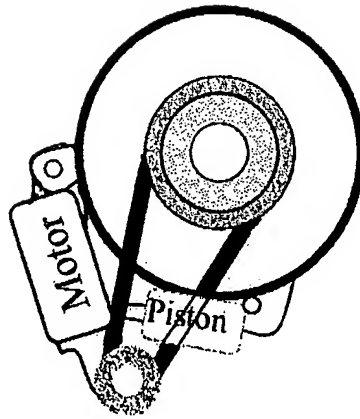


FIG. 20D

ASP700C

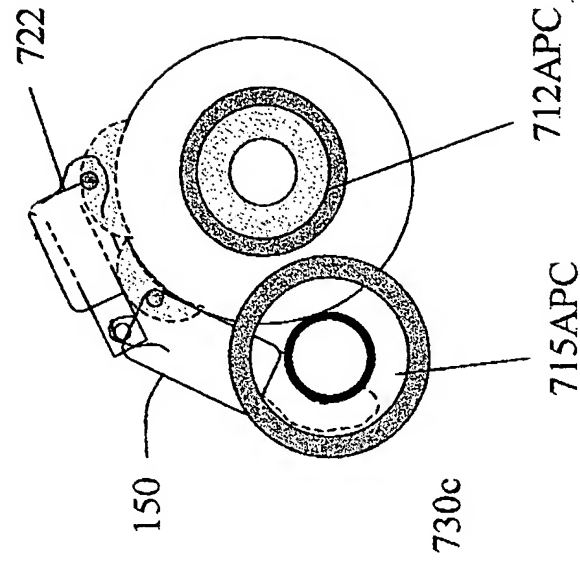


FIG. 20E

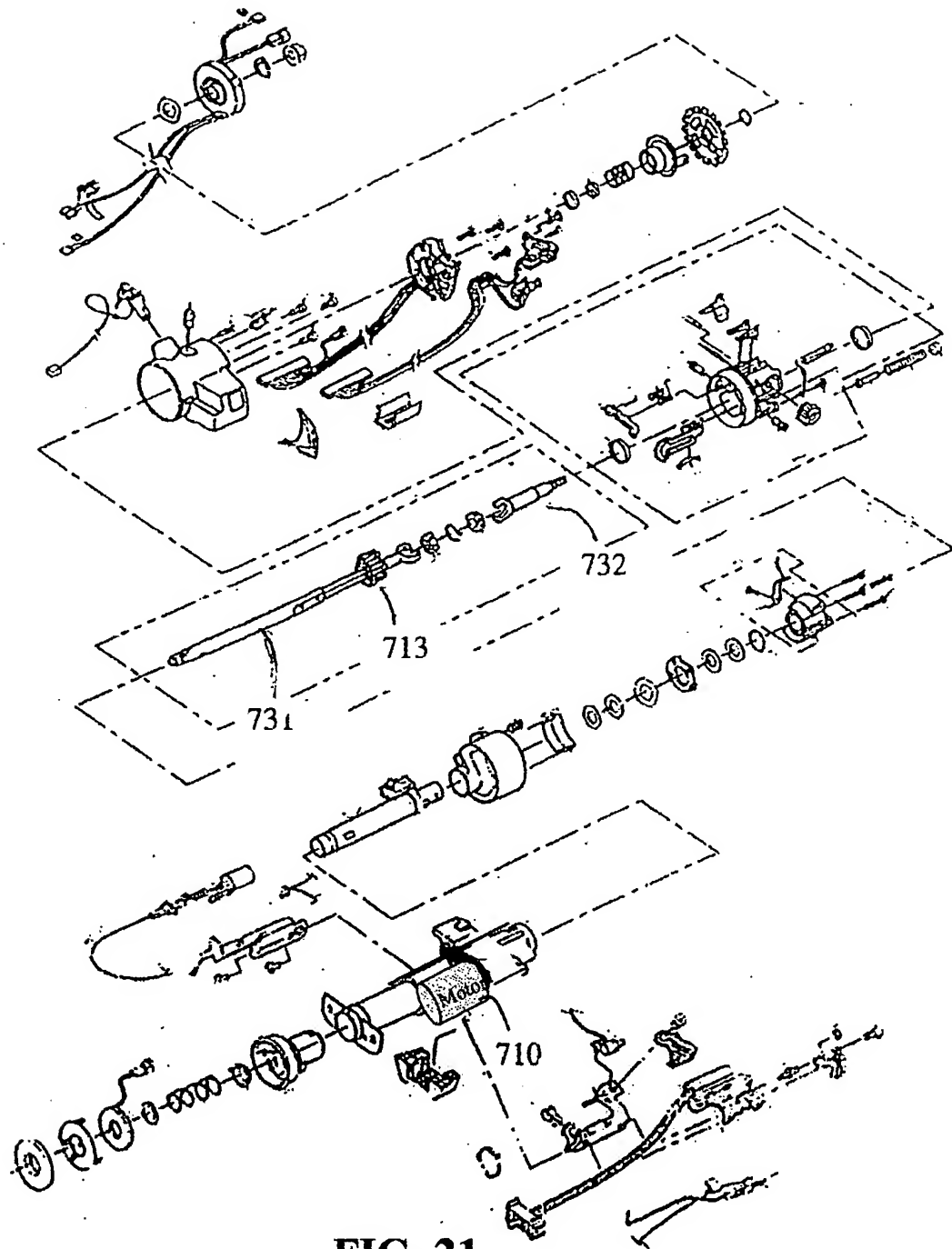


FIG. 21

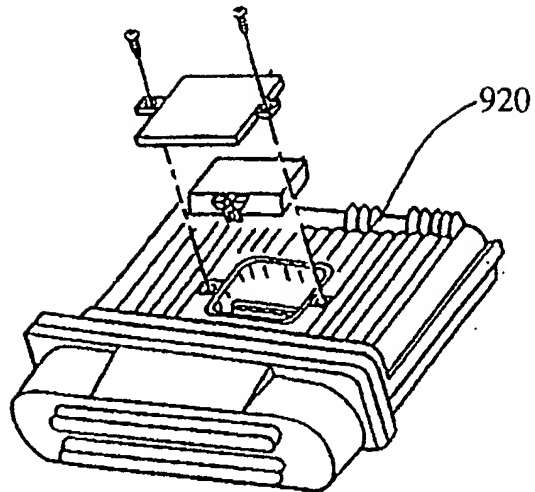


FIG. 22A

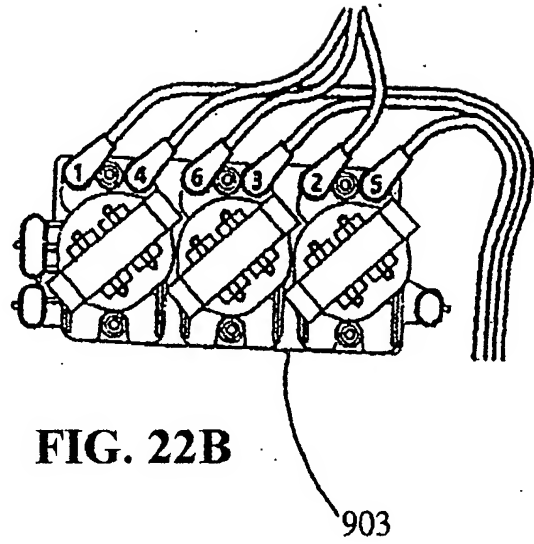


FIG. 22B

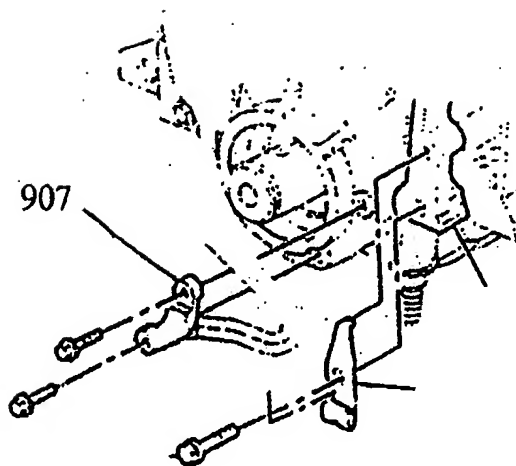
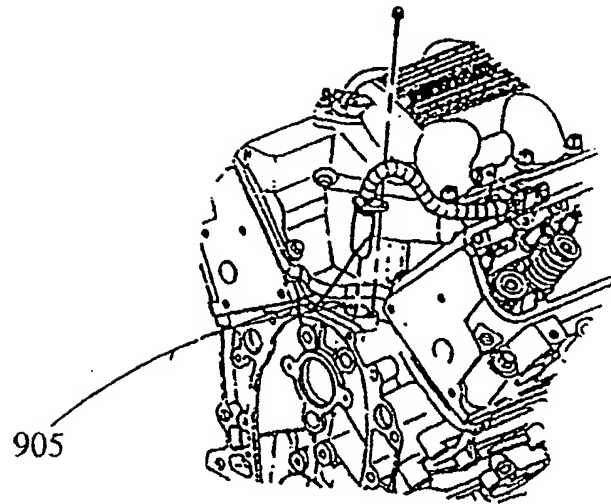
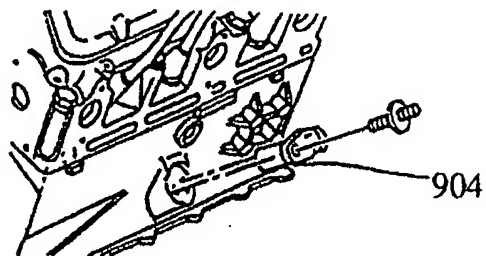


FIG. 22C



**FIG. 23A**



**FIG. 23 B**

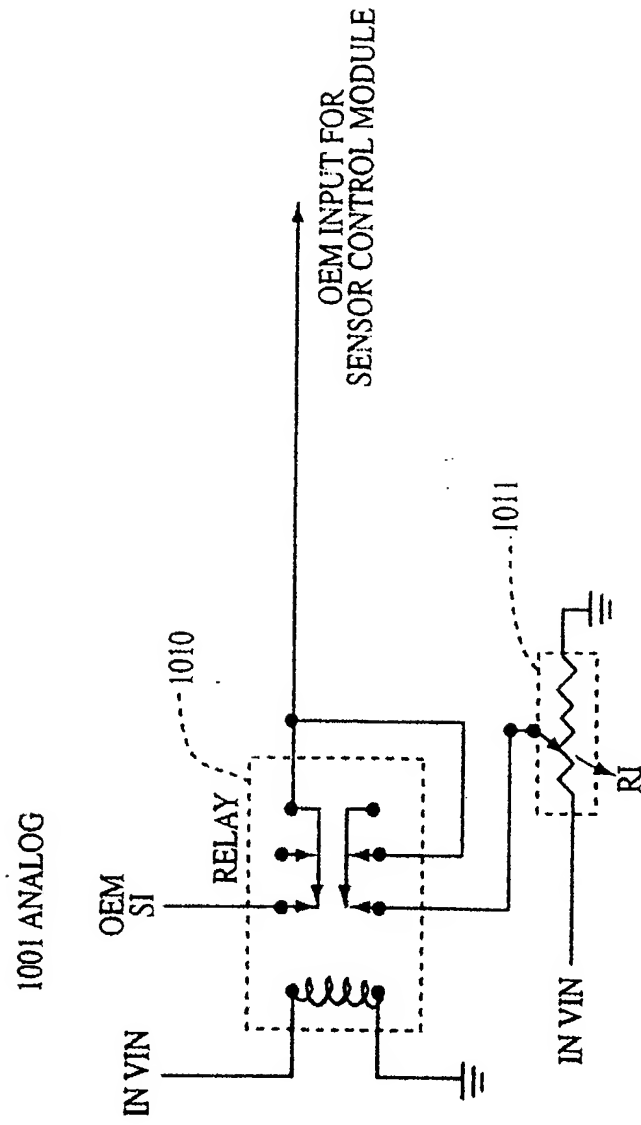


FIG. 24A

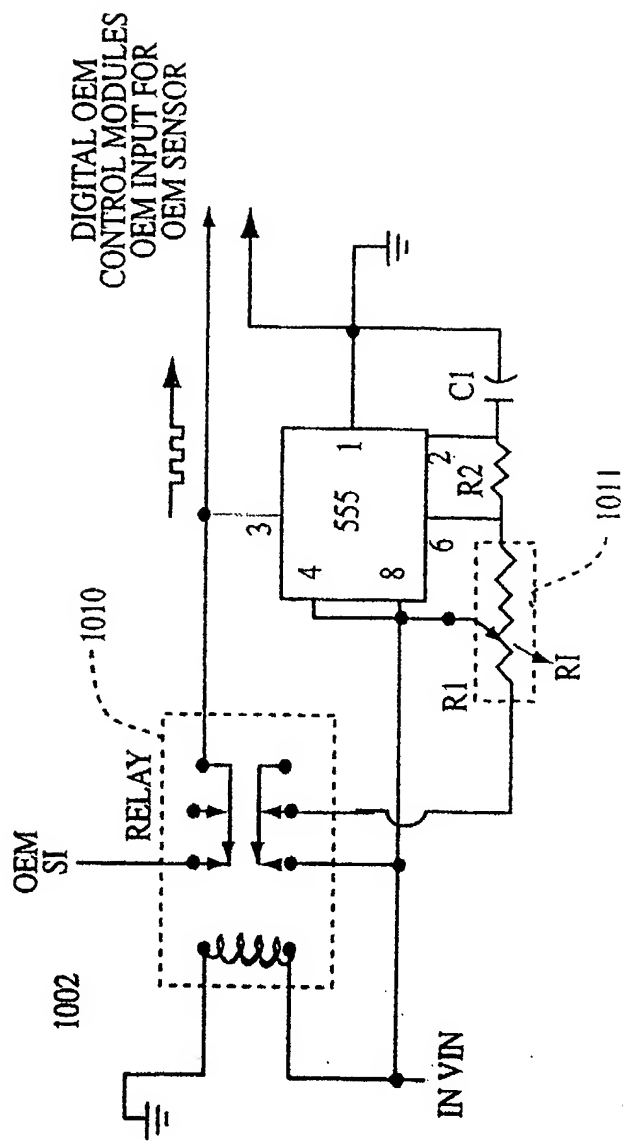


FIG. 24B



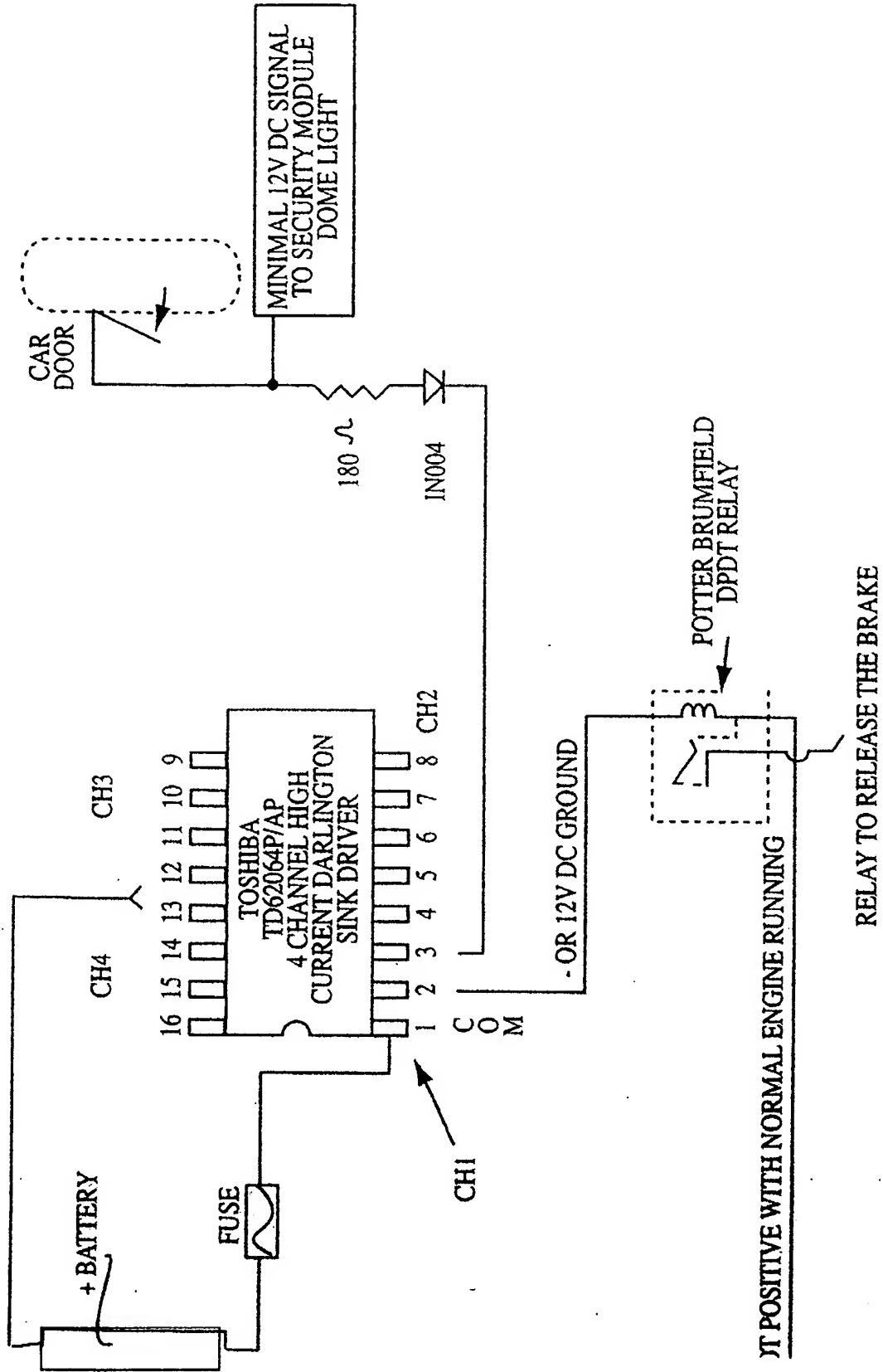


FIG. 25

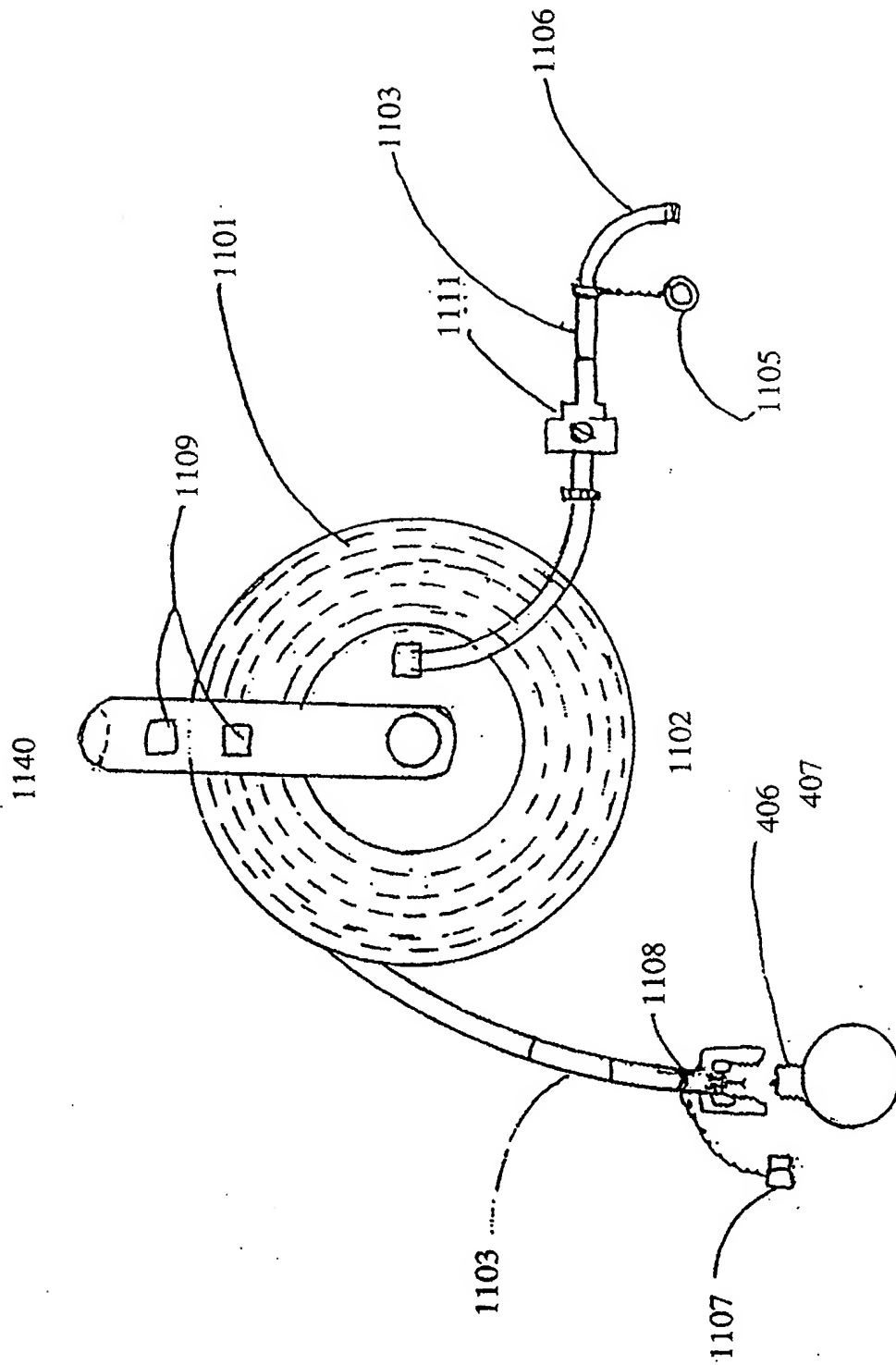


FIG. 26

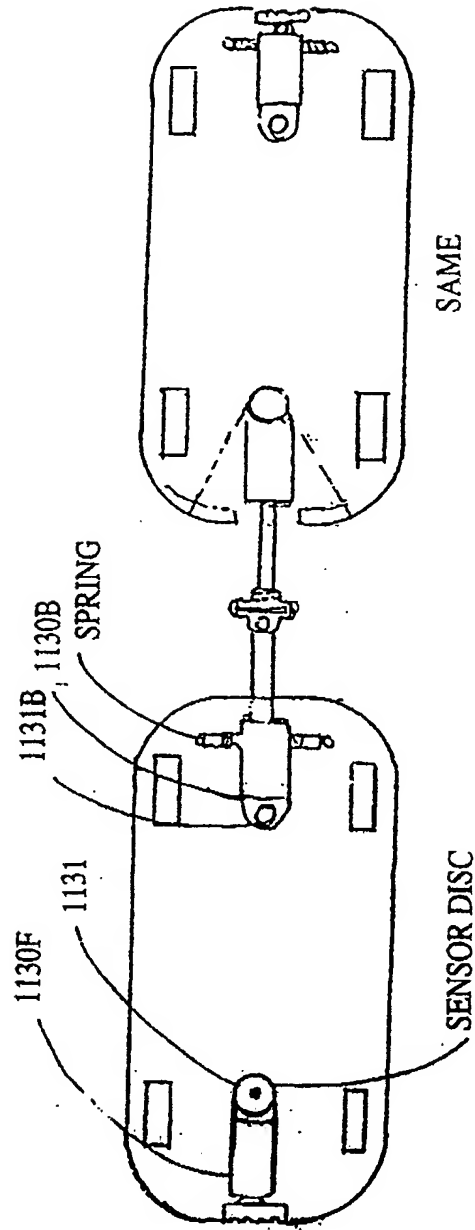


FIG. 27

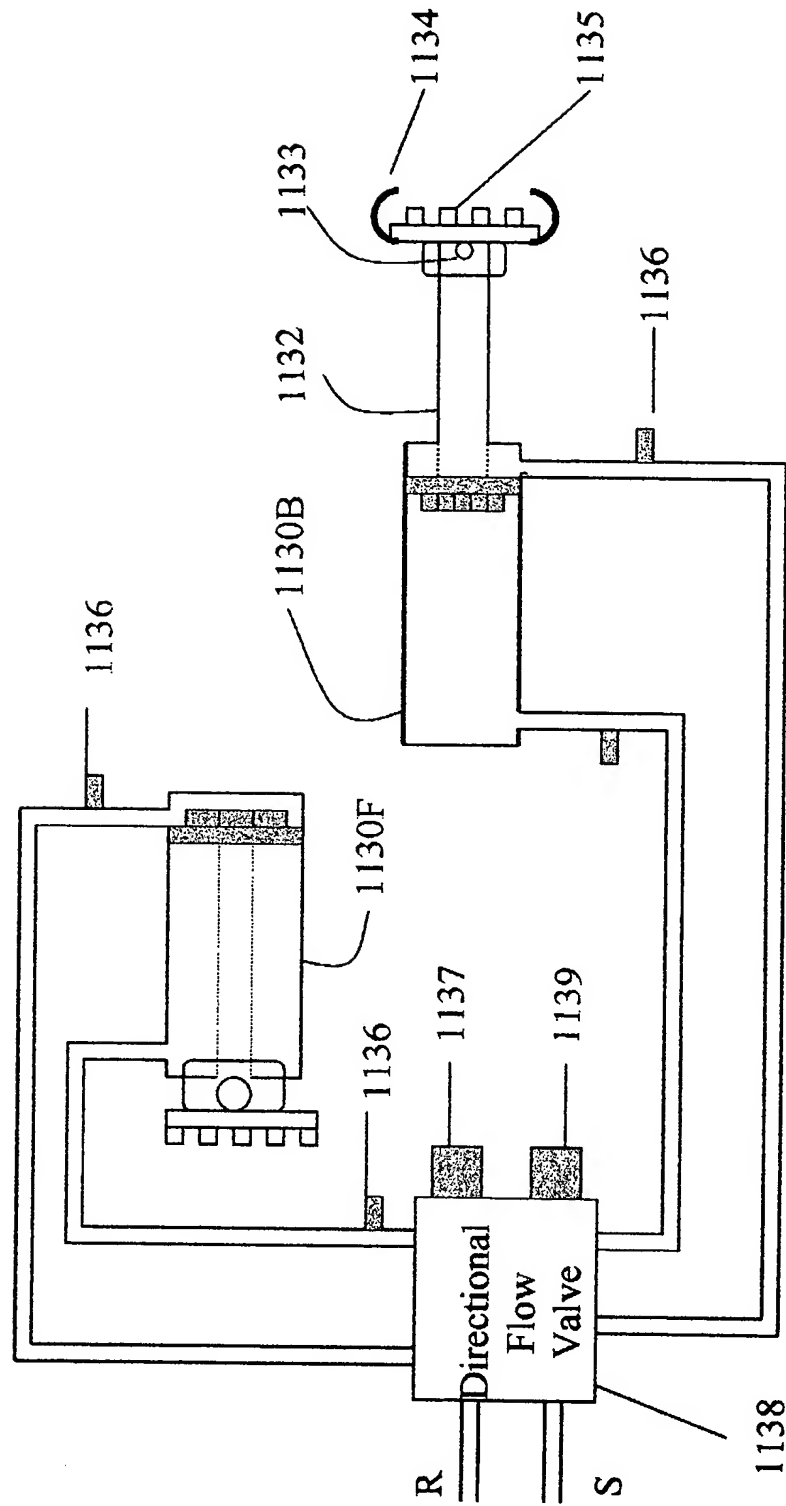
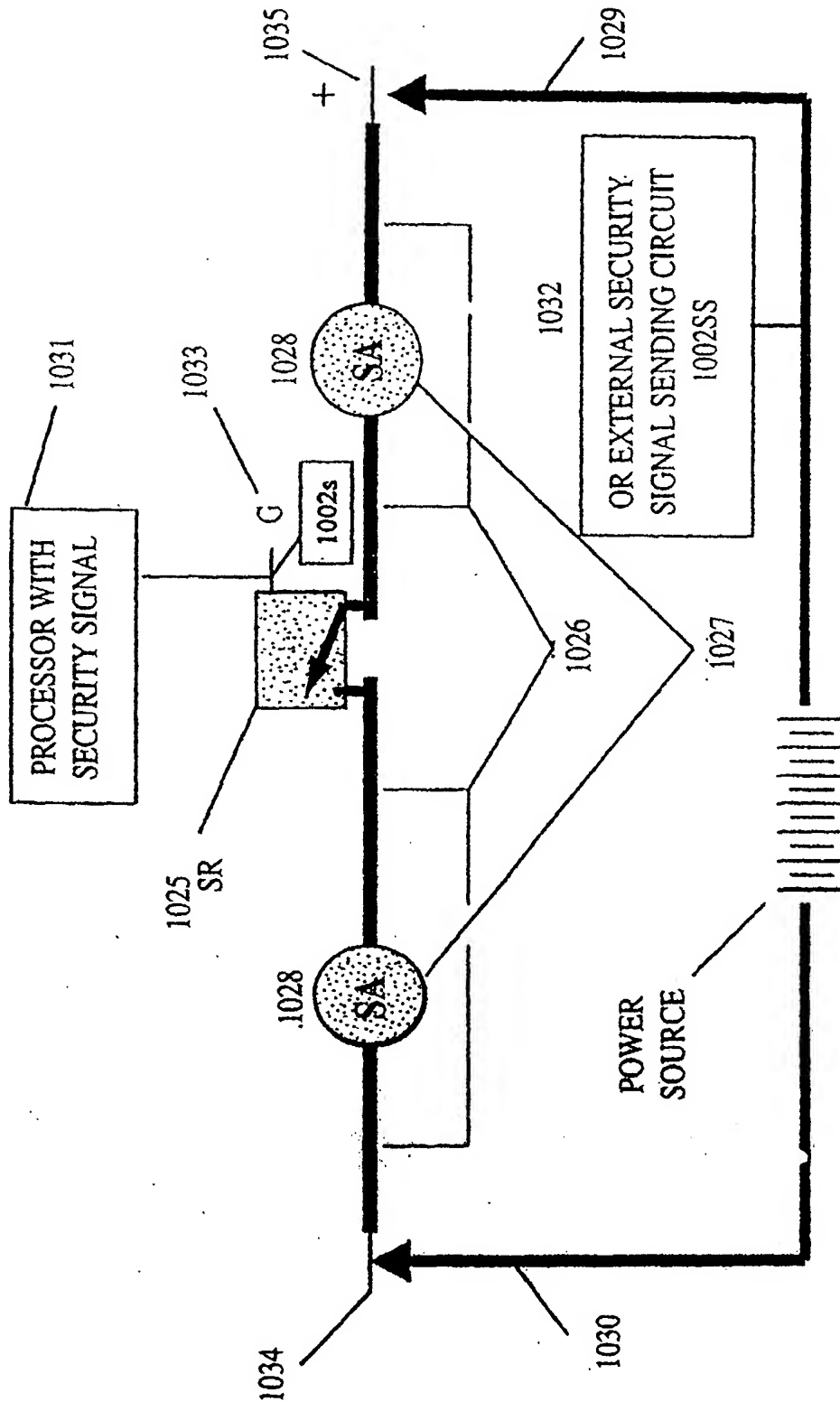


FIG. 28



**FIG. 29**

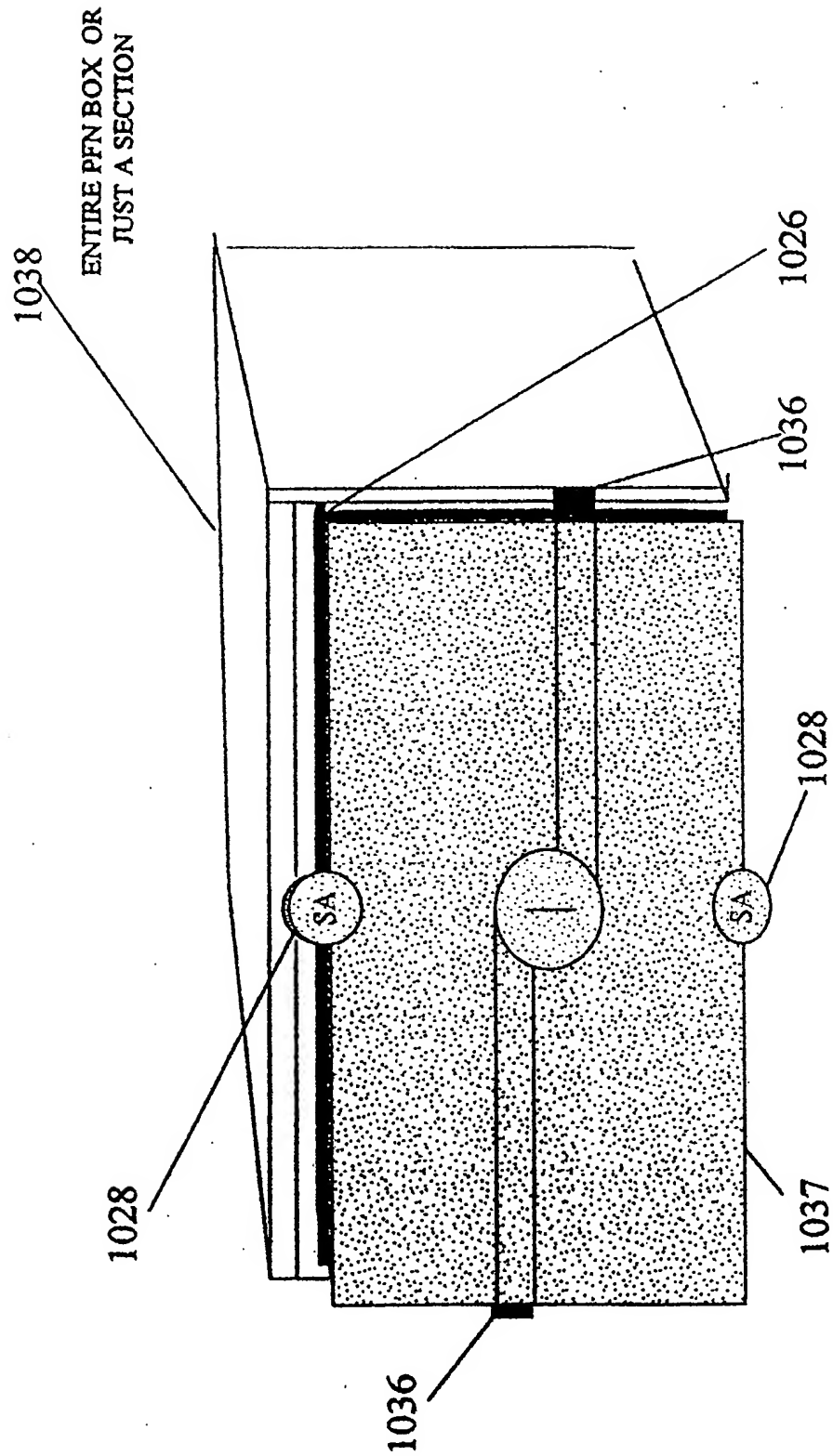


FIG. 30